ANNA UNIVERSITY, CHENNAI

AFFILIATED INSTITUTIONS

R-2014

B.E. ELECTRONICS AND COMMUNICATION ENGINEERING (PART – TIME)

I - VII SEMESTER CURRICULUM AND SYLLABUS

SEMESTER I

SL. NO.	COURSE CODE	COURSE TITLE	L	T	P	C
THEORY						
1.	PTMA6151	Applied Mathematics	3	0	0	3
2.	PTPH6151	Applied Physics	3	0	0	3
3.	PTEE6201	Circuit Theory	3	0	0	3
4.	PTEC6201	Electronic Devices	3	0	0	3
PRACTICAL						
5.	PTEC6211	Circuits and Devices Laboratory	0	0	3	2
		TOTAL	**12**	**0**	**3**	**14**

SEMESTER II

SL. No.	COURSE CODE	COURSE TITLE	L	T	P	C
THEORY						
1.	PTMA6351	Transforms and Partial Differential Equations	3	0	0	3
2.	PTEC6302	Digital Electronics	3	0	0	3
3.	PTEC6303	Signals and Systems	3	1	0	4
4.	PTEC6304	Electronic Circuits- I	3	1	0	4
PRACTICAL						
5.	PTEC6311	Analog and Digital Circuits Laboratory	0	0	3	2
		TOTAL	**12**	**2**	**3**	**16**

SEMESTER III

SL. NO.	COURSE CODE	COURSE TITLE	L	T	P	C
THEORY						
1.	PTEC6401	Electronic Circuits II	3	0	0	3
2.	PTEC6402	Communication Theory	3	0	0	3
3.	PTEC6403	Electromagnetic Fields	3	1	0	4
4.	PTEC6404	Linear Integrated Circuits	3	0	0	3
PRACTICAL						
5.	PTEC6412	Linear Integrated Circuit Laboratory	0	0	3	2
		TOTAL	**12**	**1**	**3**	**15**

SEMESTER IV

SL. NO.	COURSE CODE	COURSE TITLE	L	T	P	C
THEORY						
1.	PTEC6501	Digital Communication	3	0	0	3
2.	PTEC6502	Principles of Digital Signal Processing	3	1	0	4
3.	PTEC6503	Transmission Lines and Wave Guides	3	1	0	4
4.	PTEC6504	Microprocessor and Microcontroller	3	0	0	3
PRACTICAL						
5.	PTEC6513	Microprocessor and Microcontroller Laboratory	0	0	3	2
		TOTAL	**12**	**2**	**3**	**16**

SEMESTER V

SL. NO.	COURSE CODE	COURSE TITLE	L	T	P	C
THEORY						
1.	PTMG6851	Principles of Management	3	0	0	3
2.	PTCS6303	Computer Architecture	3	0	0	3
3.	PTEC6601	VLSI Design	3	0	0	3
4.	PTEC6602	Antenna and Wave Propagation	3	0	0	3
PRACTICAL						
5.	PTEC6612	VLSI Design Laboratory	0	0	3	2
		TOTAL	**12**	**0**	**3**	**14**

SEMESTER VI

SL. NO.	COURSE CODE	COURSE TITLE	L	T	P	C
THEORY						
1.	PTEC6801	Wireless Communication	3	0	0	3
2.	PTEC6702	Optical Communication and Networks	3	0	0	3
3.		Elective I	3	0	0	3
4.		Elective II	3	0	0	3
PRACTICAL						
5.	PTEC6712	Optical and Microwave Laboratory	0	0	3	2
		TOTAL	**12**	**0**	**3**	**14**

SEMESTER VII

SL. NO.	COURSE CODE	COURSE TITLE	L	T	P	C
THEORY						
1.	PTEC6802	Wireless Networks	3	0	0	3
2.		Elective III	3	0	0	3
3.		Elective IV	3	0	0	3
PRACTICAL						
4.	PTEC6811	Project Work	0	0	9	6
		TOTAL	**9**	**0**	**9**	**15**

TOTAL CREDITS: 104

SEMESTER VI

ELECTIVE – I

SL. NO.	CODE NO.	COURSE TITLE	L	T	P	C
1.	PTEC6001	Medical Electronics	3	0	0	3
2.	PTEC6002	Advanced Digital Signal Processing	3	0	0	3
3.	PTIT6005	Digital Image Processing	3	0	0	3
4.	PTGE6075	Professional Ethics in Engineering	3	0	0	3
5.	PTGE6083	Disaster Management	3	0	0	3

ELECTIVES– II

SL. NO.	CODE NO	COURSE TITLE	L	T	P	C
6.	PTEC6703	Embedded and Real time Systems	3	0	0	3
7.	PTEC6013	Advanced Microprocessors and Microcontrollers	3	0	0	3
8.	PTEC6012	CMOS Analog IC Design	3	0	0	3
9.	PTEC6003	Robotics and Automation	3	0	0	3
10.	PTCS6013	Foundation Skills in Integrated Product Development	3	0	0	3

SEMESTER VII

ELECTIVES– III

SL. NO.	CODE NO.	COURSE TITLE	L	T	P	C
11.	PTCS6003	Ad Hoc and Sensors Networks	3	0	0	3
12.	PTEC6009	Advanced Computer Architecture	3	0	0	3
13.	PTCS6012	Soft Computing	3	0	0	3
14.	PTEC6019	Data Converters	3	0	0	3
15.	PTGE6084	Human Rights	3	0	0	3

ELECTIVES – IV

SL. NO.	CODE NO.	COURSE TITLE	L	T	P	C
16.	PTEC6007	Speech Processing	3	0	0	3
17.	PTEC6011	Electromagnetic Interference and Compatibility	3	0	0	3
18.	PTEC6018	Multimedia Compression and Communication	3	0	0	3
19.	PTEC6004	Satellite Communication	3	0	0	3

PTMA6151 **APPLIED MATHEMATICS** **L T P C**
3 0 0 3

OBJECTIVES:

- To facilitate the understanding of the principles and to cultivate the art of formulating physical problems in the language of mathematics.

UNIT I MATRICES **9**

Characteristic equation – Eigenvalues and Eigenvectors of a real matrix – Properties of eigenvalues and eigenvectors – Cayley-Hamilton Theorem – Diagonalization of matrices - Reduction of a quadratic form to canonical form by orthogonal transformation.

UNIT II FUNCTIONS OF SEVERAL VARIABLES **9**

Partial derivatives – Homogeneous functions and Euler's theorem – Total derivative – Differentiation of implicit functions – Change of variables – Jacobians – Partial differentiation of implicit functions – Taylor's series for functions of two variables - Maxima and minima of functions of two variables.

UNIT III ANALYTIC FUNCTION **9**

Analytic functions – Necessary and sufficient conditions for analyticity – Properties – Harmonic conjugates – Construction of analytic function – Conformal Mapping – Mapping by functions w = a + z , az, 1/z, **-** Bilinear transformation.

UNIT IV COMPLEX INTEGRATION **9**

Line Integral – Cauchy's theorem and integral formula – Taylor's and Laurent's Series – Singularities – Residues – Residue theorem – Application of Residue theorem for evaluation of real integrals – Use of circular contour and semicircular contour with no pole on real axis.

UNIT V LAPLACE TRANSFORMS **9**

Existence conditions – Transforms of elementary functions – Basic properties – Transforms of derivatives and integrals –Inverse transforms – Convolution theorem – Transform of periodic functions – Application to solution of linear ordinary differential equations with constant coefficients.

TOTAL: 45 PERIODS

OUTCOMES

- To develop the use of matrix algebra techniques this is needed by engineers for practical applications.
- To familiarize the student with functions of several variables. This is needed in many branches of engineering.
- To develop an understanding of the standard techniques of complex variable theory so as to enable the student to apply them with confidence, in application areas such as heat conduction, elasticity, fluid dynamics and flow the of electric current.
- To make the student appreciate the purpose of using transforms to create a new domain in which it is easier to handle the problem that is being investigated.

TEXT BOOKS

1. Grewal B.S., Higher Engineering Mathematics, Khanna Publishers, Forty Second Edition, Delhi, 2012.
2. Ramana, B.V. Higher Engineering Mathematics" Tata McGraw Hill Publishing Company, 2008.

REFERENCES:

1. Glyn James, Advanced Modern Engineering Mathematics, Prentice Hall of India, Fouth Edition, 2011.
2. Veerarajan, T., Engineering Mathematics (For First Year), Tata McGraw-Hill Pub. Pvt. Ltd., New Delhi, 2007.

PTPH6151 **APPLIED PHYSICS** **L T P C**
3 0 0 3

OBJECTIVES:

To enrich the understanding of various types of materials and their applications in engineering and technology.

UNIT I CONDUCTING MATERIALS 9

Conductors – classical free electron theory of metals – Electrical and thermal conductivity – Wiedemann – Franz law – Lorentz number – Draw backs of classical theory – Quantum theory – Fermi distribution function – Effect of temperature on Fermi Function – Density of energy states – carrier concentration in metals.

UNIT II SEMICONDUCTING MATERIALS 9

Intrinsic semiconductor – carrier concentration derivation – Fermi level – Variation of Fermi level with temperature – electrical conductivity – band gap determination – compound semiconductors -direct and indirect band gap- derivation of carrier concentration in n-type and p-type semiconductor – variation of Fermi level with temperature and impurity concentration –– Hall effect –Determination of Hall coefficient – Applications.

UNIT III MAGNETIC AND SUPERCONDUCTING MATERIALS 9

Origin of magnetic moment – Bohr magneton – comparison of Dia, Para and Ferro magnetism – Domain theory – Hysteresis – soft and hard magnetic materials – antiferromagnetic materials – Ferrites and its applications

Superconductivity : properties – Type I and Type II superconductors – BCS theory of superconductivity(Qualitative) - High T_c superconductors – Applications of superconductors – SQUID, cryotron, magnetic levitation.

UNIT IV DIELECTRIC MATERIALS 9

Electrical susceptibility – dielectric constant – electronic, ionic, orientational and space charge polarization – frequency and temperature dependence of polarisation – internal field – Claussius – Mosotti relation (derivation) – dielectric loss – dielectric breakdown – uses of dielectric materials (capacitor and transformer) – ferroelectricity and applications.

UNIT V ADVANCED ENGINEERING MATERIALS 9

Metallic glasses: preparation, properties and applications. Shape memory alloys (SMA): Characteristics, properties of NiTi alloy, application, Nanomaterials– Preparation -pulsed laser deposition – chemical vapour deposition – Applications – NLO materials –Birefringence- optical Kerr effect – Classification of Biomaterials and its applications

TOTAL : 45 PERIODS

OUTCOMES:

- The students will be able to understand the fundamentals of materials and their applications in Engineering and Technology.

TEXT BOOKS:

1. M. Arumugam, Materials Science, Anuradha publishers, 2010
2. S.O. Pillai, Solid State Physics New Age International(P) Ltd., publishers, 2009

REFERENCES:

1. P.K. Palanisamy, Materials Science, SCITECH Publishers, 2011
2. G. Senthilkumar, Engineering Physics II, VRB Publishers, 2011
3. P. Mani, Engineering Physics II, Dhanam Publications, 2011
4. A. Marikani, Engineering Physics, PHI Learning Pvt., India, 2009

PTEE6201 **CIRCUIT THEORY** **L T P C**

(Common to branches under Electrical Faculty, ECE, Bio medical Electronics and Medical Electronics) **3 0 0 3**

OBJECTIVES:

- To introduce electric circuits and its analysis
- To impart knowledge on solving circuits using network theorems
- To introduce the phenomenon of resonance in coupled circuits.
- To educate on obtaining the transient response of circuits.
- To Phasor diagrams and analysis of three phase circuits.

UNIT I BASIC CIRCUITS ANALYSIS 9

Ohm's Law – Kirchoffs laws – DC and AC Circuits – Resistors in series and parallel circuits – Mesh current and node voltage method of analysis for D.C and A.C. circuits – Phasor Diagram – Power, Power Factor and Energy

UNIT II NETWORK REDUCTION AND NETWORK THEOREMS FOR DC AND AC CIRCUITS 9

Network reduction: voltage and current division, source transformation – star delta conversion. Thevenins and Novton & Theorem – Superposition Theorem – Maximum power transfer theorem – Reciprocity Theorem.

UNIT III RESONANCE AND COUPLED CIRCUITS 9

Series and paralled resonance – their frequency response – Quality factor and Bandwidth - Self and mutual inductance – Coefficient of coupling – Tuned circuits – Single tuned circuits.

UNIT IV TRANSIENT RESPONSE FOR DC CIRCUITS 9

Transient response of RL, RC and RLC Circuits using Laplace transform for DC input and A.C. with sinusoidal input – Characterization of two port networks in terms of Z,Y and h parameters.

UNIT V THREE PHASE CIRCUITS 9

Three phase balanced / unbalanced voltage sources – analysis of three phase 3-wire and 4-wire circuits with star and delta connected loads, balanced & un balanced – phasor diagram of voltages and currents – power and power factor measurements in three phase circuits.

TOTAL: 45 PERIODS

OUTCOMES:

- Ability analyse electrical circuits
- Ability to apply circuit theorems
- Ability to analyse AC and DC Circuits

TEXT BOOKS:

1. William H. Hayt Jr, Jack E. Kemmerly and Steven M. Durbin, "Engineering Circuits Analysis", Tata McGraw Hill publishers, 6th edition, New Delhi, 2003.
2. Joseph A. Edminister, Mahmood Nahri, "Electric circuits", Schaum's series, Tata McGraw-Hill, New Delhi, 2001.

REFERENCES:

1. Paranjothi SR, "Electric Circuits Analysis," New Age International Ltd., New Delhi, (1996).
2. Sudhakar A and Shyam Mohan SP, "Circuits and Network Analysis and Synthesis",Tata McGraw Hill, (2007).
3. Chakrabati A, "Circuits Theory (Analysis and synthesis), Dhanpath Rai & Sons, New Delhi, (1999).
4. Charles K. Alexander, Mathew N.O. Sadiku, "Fundamentals of Electric Circuits", Second Edition, McGraw Hill, (2003).

PTEC6201 **ELECTRONIC DEVICES** **L T P C**
3 0 0 3

OBJECTIVES:

- To acquaint the students with the construction, theory and operation of the basic electronic devices such as PN junction diode, Bipolar and Field effect Transistors, Power control devices, LED, LCD and other Opto-electronic devices

UNIT I SEMICONDUCTOR DIODE **9**

PN junction diode, Current equations, Diffusion and drift current densities, forward and reverse bias characteristics, Switching Characteristics.

UNIT II BIPOLAR JUNCTION TRANSISTOR **9**

NPN -PNP -Junctions-Early effect-Current equations – Input and Output characteristics of CE, CB CC-Hybrid -π model - h-parameter model, Ebers Moll Model- Gummel Poon-model, Multi Emitter Transistor.

UNIT III FIELD EFFECT TRANSISTORS **9**

JFETs – Drain and Transfer characteristics,-Current equations-Pinch off voltage and its significance-MOSFET- Characteristics- Threshold voltage -Channel length modulation, D-MOSFET, E-MOSFET-, Current equation - Equivalent circuit model and its parameters, FINFET,DUAL GATE MOSFET.

UNIT IV SPECIAL SEMICONDUCTOR DEVICES **9**

Metal-Semiconductor Junction- MESFET, Schottky barrier diode-Zener diode-Varactor diode –Tunnel diode- Gallium Arsenide device, LASER diode, LDR.

UNIT V POWER DEVICES AND DISPLAY DEVICES **9**

UJT, SCR, Diac, Triac, Power BJT- Power MOSFET- DMOS-VMOS. LED, LCD, Photo transistor, Opto Coupler, Solar cell, CCD.

TOTAL : 45 PERIODS

OUTCOMES:

At the end of the course, the student should be able to:

- Explain the theory, construction, and operation of basic electronic devices.
- Use the basic electronic devices

TEXT BOOK:

1. Donald A Neaman, “Semiconductor Physics and Devices”, Third Edition, Tata Mc Graw Hill Inc. 2007.

REFERENCES:

1. Yang, “Fundamentals of Semiconductor devices”, McGraw Hill International Edition, 1978.
2. Robert Boylestad and Louis Nashelsky, “Electron Devices and Circuit Theory” Pearson Prentice Hall, 10th edition,July 2008.

PTEC6211 **CIRCUITS AND DEVICES LABORATORY** **L T P C**
0 0 3 2

OBJECTIVES:

The student should be made to:

- Be exposed to the characteristics of basic electronic devices
- Be exposed to RL and RC circuits
- Be familiar with Thevinin & Norton theorem KVL & KCL, and Super Position Theorems

LIST OF EXPERIMENTS:

1. Characteristics of PN Junction Diode
2. Zener diode Characteristics & Regulator using Zener diode
3. Common Emitter input-output Characteristics
4. Common Base input-output Characteristics
5. FET Characteristics
6. SCR Characteristics
7. Clipper and Clamper & FWR
8. Verifications Of Thevinin & Norton theorem
9. Verifications Of KVL & KCL
10. Verifications Of Super Position Theorem
11. verifications of maximum power transfer & reciprocity theorem
12. Determination Of Resonance Frequency of Series & Parallel RLC Circuits
13. Transient analysis of RL and RC circuits

TOTAL: 45 PERIODS

OUTCOMES:

At the end of the course, the student should be able to:

- Learn the characteristics of basic electronic devices
- Design RL and RC circuits
- Verify Thevinin & Norton theorem KVL & KCL, and Super Position Theorems

LIST OF EQUIPMENTS FOR A BATCH OF 30 STUDENTS

BC 107, BC 148,2N2646,BFW10	- 25 each
1N4007, Zener diodes	- 25 each
Resistors, Capacitors, Inductors	- sufficient quantities
Bread Boards	- 15 Nos
CRO (30MHz)	– 10 Nos.
Function Generators (3MHz)	– 10 Nos.
Dual Regulated Power Supplies (0 – 30V)	– 10 Nos.

PTMA6351 TRANSFORMS AND PARTIAL DIFFERENTIAL EQUATIONS L T P C
3 0 0 3

OBJECTIVES:

- To facilitate the understanding of the principles and to cultivate the art of formulating physical problems in the language of mathematics.

UNIT I FOURIER SERIES 9

Dirichlet's conditions – General Fourier series – Odd and even functions – Half-range Sine and Cosine series –Parseval's identity – Harmonic Analysis.

UNIT II FOURIER TRANSFORM 9

Fourier integral theorem – Fourier transform pair-Sine and Cosine transforms – Properties – Transform of elementary functions – Convolution theorem – Parseval's identity.

UNIT III PARTIAL DIFFERENTIAL EQUATIONS 9

Formation – Solutions of first order equations – Standard types and Equations reducible to standard types – Singular solutions – Lagrange's Linear equation –Solution of homogenous linear equations of higher order with constant coefficients.

UNIT IV APPLICATIONS OF PARTIAL DIFFERENTIAL EQUATIONS 9

Method of separation of Variables – Solutions of one dimensional wave equation and one-dimensional heat equation – Steady state solution of two-dimensional heat equation.

UNIT V Z – TRANSFORM AND DIFFERENCE EQUATIONS 9

Z-transform – Elementary properties – Inverse Z-transform – Convolution theorem –Formation of difference equation – Solution of difference equation using Z-transform.

TOTAL: 45 PERIODS

OUTCOMES:

- To introduce Fourier series analysis which is central to many applications in engineering apart from its use in solving boundary value problems.
- To acquaint the student with Fourier transform techniques used in wide variety of situations in which the functions used are not periodic.
- To introduce the effective mathematical tools for the solutions of partial differential equations that model physical processes.
- To develop Z- transform techniques which will perform the same task for discrete time systems as Laplace Transform, a valuable aid in analysis of continuous time systems.

TEXT BOOK

1. Grewal B.S., Higher Engineering Mathematics, Khanna Publishers, Forty Second Edition, Delhi, 2012

REFERENCES

1. Glyn James, Advanced Modern Engineering Mathematics, Prentice Hall of India, Fourth Edition, 2011
2. Ramana, B.V. Higher Engineering Mathematics" Tata McGraw Hill Publishing Company, 2008.
3. Bali, N.P. and Manish Goyal, A Text Book of Engineering Mathematics, Lakshmi Publications Pvt. Ltd., New Delhi, 2006.

PTEC6302 DIGITAL ELECTRONICS L T P C

3 0 0 3

OBJECTIVES:

- To introduce basic postulates of Boolean algebra and shows the correlation between Boolean expressions
- To introduce the methods for simplifying Boolean expressions
- To outline the formal procedures for the analysis and design of combinational circuits
- and sequential circuits
- To introduce the concept of memories and programmable logic devices.
- To illustrate the concept of synchronous and asynchronous sequential circuits

UNIT I MINIMIZATION TECHNIQUES AND LOGIC GATES 9

Minimization Techniques: Boolean postulates and laws – De-Morgan's Theorem - Principle of Duality - Boolean expression - Minimization of Boolean expressions –– Minterm – Maxterm - Sum of Products (SOP) – Product of Sums (POS) – Karnaugh map Minimization – Don't care conditions – Quine - Mc Cluskey method of minimization.

Logic Gates: AND, OR, NOT, NAND, NOR, Exclusive–OR and Exclusive–NOR Implementations of Logic Functions using gates, NAND–NOR implementations – Multi
level gate implementations- Multi output gate implementations. TTL and CMOS Logic and their characteristics – Tristate gates

UNIT II COMBINATIONAL CIRCUITS 9

Design procedure – Half adder – Full Adder – Half subtractor – Full subtractor – Parallel binary adder, parallel binary Subtractor – Fast Adder - Carry Look Ahead adder – Serial Adder/Subtractor - BCD adder – Binary Multiplier – Binary Divider - Multiplexer/ Demultiplexer – decoder - encoder – parity checker – parity generators – code converters - Magnitude Comparator.

UNIT III SEQUENTIAL CIRCUITS 9

Latches, Flip-flops - SR, JK, D, T, and Master-Slave – Characteristic table and equation –Application table – Edge triggering – Level Triggering – Realization of one flip flop using other flip flops – serial adder/subtractor- Asynchronous Ripple or serial counter – Asynchronous Up/Down counter - Synchronous counters – Synchronous Up/Down counters – Programmable counters – Design of Synchronous counters: state diagram- State table –State minimization –State assignment - Excitation table and maps-Circuit implementation - Modulo–n counter, Registers – shift registers - Universal shift registers – Shift register counters – Ring counter – Shift counters - Sequence generators.

UNIT IV MEMORY DEVICES 9

Classification of memories – ROM - ROM organization - PROM – EPROM – EEPROM –EAPROM, RAM – RAM organization – Write operation – Read operation – Memory cycle - Timing wave forms – Memory decoding – memory expansion – Static RAM Cell- Bipolar RAM cell – MOSFET RAM cell – Dynamic RAM cell –Programmable Logic Devices – Programmable Logic Array (PLA) - Programmable Array Logic (PAL) – Field Programmable Gate Arrays (FPGA) - Implementation of combinational logic circuits using ROM, PLA, PAL

UNIT V SYNCHRONOUS AND ASYNCHRONOUS SEQUENTIAL CIRCUITS 9

Synchronous Sequential Circuits: General Model – Classification – Design – Use of Algorithmic State Machine – Analysis of Synchronous Sequential Circuits

Asynchronous Sequential Circuits: Design of fundamental mode and pulse mode circuits – Incompletely specified State Machines – Problems in Asynchronous Circuits – Design of Hazard Free Switching circuits. Design of Combinational and Sequential circuits using VERILOG.

TOTAL: 45 PERIODS

OUTCOMES:

Students will be able to:

- Analyze different methods used for simplification of Boolean expressions.
- Design and implement Combinational circuits.
- Design and implement synchronous and asynchronous sequential circuits.
- Write simple HDL codes for the circuits.

TEXT BOOK:

1. M. Morris Mano, “Digital Design”, 4th Edition, Prentice Hall of India Pvt. Ltd., 2008 / Pearson Education (Singapore) Pvt. Ltd., New Delhi, 2003.

REFERENCES:

1. John F.Wakerly, “Digital Design”, Fourth Edition, Pearson/PHI, 2008
2. John.M Yarbrough, “Digital Logic Applications and Design”, Thomson Learning, 2006.
3. Charles H.Roth. “Fundamentals of Logic Design”, 6th Edition, Thomson Learning, 2013.
4. Donald P.Leach and Albert Paul Malvino, “Digital Principles and Applications”, 6th Edition, TMH, 2006.
5. Thomas L. Floyd, “Digital Fundamentals”, 10th Edition, Pearson Education Inc, 2011
6. Donald D.Givone, “Digital Principles and Design”, TMH, 2003.

PTEC6303 **SIGNALS AND SYSTEMS** **L T P C**
3 1 0 4

OBJECTIVES:

- To understand the basic properties of signal & systems and the various methods of classification
- To learn Laplace Transform &Fourier transform and their properties
- To know Z transform & DTFT and their properties
- To characterize LTI systems in the Time domain and various Transform domains

UNIT I CLASSIFICATION OF SIGNALS AND SYSTEMS 9

Continuous time signals (CT signals) - Discrete time signals (DT signals) - Step, Ramp, Pulse, Impulse, Sinusoidal, Exponential, Classification of CT and DT signals - Periodic & Aperiodic signals, Deterministic & Random signals, Energy & Power signals - CT systems and DT systems- Classification of systems – Static & Dynamic, Linear & Nonlinear, Time-variant & Time-invariant, Causal & Noncausal, Stable & Unstable.

UNIT II ANALYSIS OF CONTINUOUS TIME SIGNALS 9

Fourier series analysis-spectrum of Continuous Time (CT) signals- Fourier and Laplace Transforms in CT Signal Analysis - Properties.

UNIT III LINEAR TIME INVARIANT- CONTINUOUS TIME SYSTEMS 9

Differential Equation-Block diagram representation-impulse response, convolution integrals-Fourier and Laplace transforms in Analysis of CT systems

UNIT IV ANALYSIS OF DISCRETE TIME SIGNALS 9

Baseband Sampling - DTFT – Properties of DTFT - Z Transform – Properties of Z Transform

UNIT V LINEAR TIME INVARIANT-DISCRETE TIME SYSTEMS 9

Difference Equations-Block diagram representation-Impulse response - Convolution sum- Discrete Fourier and Z Transform Analysis of Recursive & Non-Recursive systems

TOTAL (L:45+T:15): 60 PERIODS

OUTCOMES:

Upon the completion of the course, students will be able to:

- Analyze the properties of signals & systems
- Apply Laplace transform, Fourier transform, Z transform and DTFT in signal analysis
- Analyze continuous time LTI systems using Fourier and Laplace Transforms
- Analyze discrete time LTI systems using Z transform and DTFT

TEXT BOOK:

1. Allan V.Oppenheim, S.Wilsky and S.H.Nawab, “Signals and Systems”, Pearson, 2007.

REFERENCES:

1. B. P. Lathi, “Principles of Linear Systems and Signals”, Second Edition, Oxford, 2009.
2. R.E.Zeimer, W.H.Tranter and R.D.Fannin, “Signals & Systems - Continuous and Discrete”, Pearson, 2007.
3. John Alan Stuller, “An Introduction to Signals and Systems”, Thomson, 2007.
4. M.J.Roberts, “Signals & Systems Analysis using Transform Methods & MATLAB”, Tata McGraw Hill, 2007.

PTEC6304 **ELECTRONIC CIRCUITS – I** **L T P C**
3 1 0 4

OBJECTIVES:

The student should be made to

- Learn about biasing of BJTs and MOSFETs
- Design and construct amplifiers
- Construct amplifiers with active loads
- Study high frequency response of all amplifiers

UNIT I POWER SUPPLIES AND BIASING OF DISCRETE BJT AND MOSFET 9

Rectifiers with filters- DC Load line, operating point, Various biasing methods for BJT-Design-Stability-Bias compensation, Thermal stability, Design of biasing for JFET, Design of biasing for MOSFET

UNIT II BJT AMPLIFIERS 9

Small signal Analysis of Common Emitter-AC Load line, Voltage swing limitations, Common collector and common base amplifiers – Differential amplifiers- CMRR- Darlington Amplifier- Bootstrap technique - Cascaded stages - Cascode Amplifier-**Large signal Amplifiers – Class A , Class B and Class C Power Amplifiers .**

UNIT III JFET AND MOSFET AMPLIFIERS 9

Small signal analysis of JFET amplifiers- Small signal Analysis of MOSFET and JFET, Common source amplifier, Voltage swing limitations, Small signal analysis of MOSFET and JFET Source follower and Common Gate amplifiers, - BiMOS Cascode amplifier

UNIT IV FREQUENCY ANALYSIS OF BJT AND MOSFET AMPLIFIERS 9

Low frequency and Miller effect, High frequency analysis of CE and MOSFET CS amplifier, Short circuit current gain, cut off frequency – fα and fβ unity gain and Determination of bandwidth of single stage and multistage amplifiers

UNIT V IC MOSFET AMPLIFIERS 9

IC Amplifiers- IC biasing Current steering circuit using MOSFET- MOSFET current sources- PMOS and NMOS current sources. Amplifier with active loads - enhancement load, Depletion load and PMOS and NMOS current sources load- CMOS common source and source follower- CMOS differential amplifier- CMRR.

TOTAL (L: 45+T: 15): 60 PERIODS

OUTCOMES:

Upon Completion of the course, the students will be able to:

Design circuits with transistor biasing.
Design simple amplifier circuits.
Analyze the small signal equivalent circuits of transistors.
Design and analyze large signal amplifiers.

TEXT BOOK:

1. Donald .A. Neamen, Electronic Circuit Analysis and Design –2nd Edition,Tata Mc Graw Hill, 2009.

REFERENCES:

1. Adel .S. Sedra, Kenneth C. Smith, “Micro Electronic Circuits”, 6th Edition, Oxford University Press, 2010.
2. David A., “Bell Electronic Devices and Circuits”, Oxford Higher Education Press, 5th Edition, 2010
3. Behzad Razavi, “Design of Analog CMOS Integrated Circuits”, Tata Mc Graw Hill, 2007.
4. Paul Gray, Hurst, Lewis, Meyer “Analysis and Design of Analog Integrated Circuits”, 4th Edition ,John Willey & Sons 2005

5. Millman.J. and Halkias C.C, “Integrated Electronics”, Mc Graw Hill, 2001.
6. D.Schilling and C.Belove, “Electronic Circuits”, 3rd Edition, Mc Graw Hill, 1989.
7. Robert L. Boylestad and Louis Nasheresky, “Electronic Devices and Circuit Theory”, 10th Edition, Pearson Education / PHI, 2008.

PTEC6311 ANALOG AND DIGITAL CIRCUITS LABORATORY L T P C
0 0 3 2

OBJECTIVES:

The student should be made to:

- Study the characteristic of CE,CB and CC Amplifier
- Learn the frequency response of CS Amplifiers
- Study the Transfer characteristic of differential amplifier
- Perform experiment to obtain the bandwidth of single stage and multistage amplifiers
- Perform SPICE simulation of **E**lectronic **C**ircuits

LIST OF ANALOG EXPERIMENTS:

1. Half Wave and Full Wave Rectifiers, Filters, Power supplies
2. Frequency Response of CE, CB, CC and CS amplifiers
3. Darlington Amplifier
4. Differential Amplifiers- Transfer characteristic, CMRR Measurement
5. Cascode / Cascade amplifier
6. Class A and Class B Power Amplifiers
7. Determination of bandwidth of single stage and multistage amplifiers
8. Spice Simulation of Common Emitter and Common Source amplifiers

LIST OF DIGITAL EXPERIMENTS

9. Design and implementation of code converters using logic gates
   (i) BCD to excess-3 code and vice versa (ii) Binary to gray and vice-versa
10. Design and implementation of 4 bit binary Adder/ Subtractor and BCD adder using IC 7483
11. Design and implementation of Multiplexer and De-multiplexer using logic gates
12. Design and implementation of encoder and decoder using logic gates
13. Construction and verification of 4 bit ripple counter and Mod-10 / Mod-12 Ripple counters
14. Design and implementation of 3-bit synchronous up/down counter
15. Implementation of SISO, SIPO, PISO and PIPO shift registers using Flip- flops.

TOTAL: 45 PERIODS

OUTCOMES:

At the end of the course, the student should be able to:

- Differentiate cascade and cascade amplifier.
- Analyze the limitation in bandwidth of single stage and multi stage amplifier
- Simulate amplifiers using Spice
- Measure CMRR in differential amplifier

LAB REQUIREMENTS FOR A BATCH OF 30 STUDENTS, 2 STUDENTS / EXPERIMENT:

Equipments for Analog Lab

CRO (30MHz)	– 15 Nos.
Signal Generator /Function Generators (3 MHz)	– 15 Nos
Dual Regulated Power Supplies (0 – 30V)	– 15 Nos.
Standalone desktop PCs with SPICE software	– 15 Nos.
Transistor/FET (BJT-NPN-PNP and NMOS/PMOS)	– 50 Nos
Components and Accessories	

Equipments for Digital Lab

Dual power supply/ single mode power supply	- 15 Nos
IC Trainer Kit	- 15 Nos
Bread Boards	- 15 Nos
Computer with HDL software	- 15 Nos
Seven segment display	-15 Nos
Multimeter	- 15 Nos

ICs each 50 Nos

7400/ 7402 / 7404 / 7486 / 7408 / 7432 / 7483 / 74150 / 74151 / 74147 / 7445 / 7476/7491/ 555 / 7494 / 7447 / 74180 / 7485 / 7473 / 74138 / 7411 / 7474

PTEC6401 ELECTRONIC CIRCUITS II L T P C
3 0 0 3

OBJECTIVES:

- To understand the advantages and method of analysis of feedback amplifiers.
- To understand the analysis and design of LC and RC oscillators, amplifiers, multivibrators, and time base generators.

UNIT I FEEDBACK AMPLIFIERS 9

General Feedback Structure – Properties of negative feedback – Basic Feedback Topologies – Feedback amplifiers – Series – Shunt, Series – Series, Shunt – Shunt and Shunt – Series Feedback – Determining the Loop Gain – Stability Problem – Nyquist Plot – Effect of feedback on amplifier poles – Frequency Compensation.

UNIT II OSCILLATORS 9

Classification, Barkhausen Criterion - Mechanism for start of oscillation and stabilization of amplitude, General form of an Oscillator, Analysis of LC oscillators - Hartley, Colpitts,Clapp, Franklin, Armstrong, Tuned collector oscillators, RC oscillators - phase shift –Wienbridge - Twin-T Oscillators, Frequency range of RC and LC Oscillators, Quartz Crystal Construction, Electrical equivalent circuit of Crystal, Miller and Pierce Crystal oscillators, frequency stability of oscillators.

UNIT III TUNED AMPLIFIERS 9

Coil losses, unloaded and loaded Q of tank circuits, small signal tuned amplifiers - Analysis of capacitor coupled single tuned amplifier – double tuned amplifier - effect of cascading single tuned and double tuned amplifiers on bandwidth – Stagger tuned amplifiers – large signal tuned amplifiers – Class C tuned amplifier – Efficiency and applications of Class C tuned amplifier - Stability of tuned amplifiers – Neutralization - Hazeltine neutralization method.

UNIT IV WAVE SHAPING AND MULTIVIBRATOR CIRCUITS 9

RC & RL Integrator and Differentiator circuits – Storage, Delay and Calculation of Transistor Switching Times – Speed-up Capaitor - Diode clippers, Diode comparator - Clampers. Collector coupled and Emitter coupled Astable multivibrator – Monostable multivibrator - Bistable multivibrators - Triggering methods for Bistable multivibrators - Schmitt trigger circuit

UNIT V BLOCKING OSCILLATORS AND TIMEBASE GENERATORS 9

UJT saw tooth waveform generator, Pulse transformers – equivalent circuit – response - applications, Blocking Oscillator – Free running blocking oscillator - Astable Blocking Oscillators with base timing – Push-pull Astable blocking oscillator with emitter timing, Frequency control using core saturation, Triggered blocking oscillator – Monostable blocking oscillator with base timing – Monostable blocking oscillator with emitter timing, Time base circuits - Voltage-Time base circuit, Current-Time base circuit – Linearization through adjustment of driving waveform.

TOTAL: 45 PERIODS

OUTCOMES:

Upon Completion of the course, the students will be able to

- Design and analyze feedback amplifiers.
- Design LC and RC oscillators, tuned amplifiers, wave shaping circuits, multivibrators, blocking oscillators and time base generators.
- Analyze performance of tuned amplifiers.

TEXT BOOK:

1. Sedra and Smith, “Micro Electronic Circuits”; Sixth Edition, Oxford University Press, 2011.

REFERENCES:

1. Robert L. Boylestad and Louis Nasheresky, “Electronic Devices and Circuit Theory”, 10th Edition, Pearson Education / PHI, 2008
2. David A. Bell, “Electronic Devices and Circuits”, Fifth Edition, Oxford University Press, 2008.
3. Millman J. and Taub H., “Pulse Digital and Switching Waveforms”, TMH, 2000.
4. Millman and Halkias. C., Integrated Electronics, TMH, 2007.

PTEC6402 COMMUNICATION THEORY L T P C
3 0 0 3

OBJECTIVES:

- To introduce the concepts of various analog modulations and their spectral characteristics
- To understand the properties of random process
- To know the effect of noise on communication systems
- To study the limits set by Information Theory

UNIT I AMPLITUDE MODULATION 9

Generation and detection of AM wave-spectra-DSBSC, Hilbert Transform, Pre-envelope & complex envelope - SSB and VSB –comparison -Superheterodyne Receiver.

UNIT II ANGLE MODULATION 9

Phase and frequency modulation-Narrow Band and Wind band FM - Spectrum - FM modulation and demodulation – FM Discriminator- PLL as FM Demodulator - Transmission bandwidth.

UNIT III RANDOM PROCESS 9

Random variables, Central limit Theorem, Random Process, Stationary Processes, Mean, Correlation & Covariance functions, Power Spectral Density, Ergodic Processes, Gaussian Process, Transmission of a Random Process Through a LTI filter.

UNIT IV NOISE CHARACTERIZATION 9

Noise sources and types – Noise figure and noise temperature – Noise in cascaded systems. Narrow band noise – PSD of in-phase and quadrature noise –Noise performance in AM systems – Noise performance in FM systems – Pre-emphasis and de-emphasis – Capture effect, threshold effect.

UNIT V INFORMATION THEORY 9

Entropy - Discrete Memoryless channels - Channel Capacity -Hartley - Shannon law - Source coding theorem - Huffman & Shannon - Fano codes

TOTAL: 45 PERIODS

OUTCOMES:

At the end of the course, the students would

- Design AM communication systems.
- Design Angle modulated communication systems
- Apply the concepts of Random Process to the design of Communication systems
- Analyze the noise performance of AM and FM systems

TEXT BOOKS:

1. J.G.Proakis, M.Salehi, “Fundamentals of Communication Systems”, Pearson Education 2006.
2. S. Haykin, “Digital Communications”, John Wiley, 2005.

REFERENCES:

1. B.P.Lathi, “Modern Digital and Analog Communication Systems”, 3rd Edition, Oxford University Press, 2007.
2. B.Sklar, “Digital Communications Fundamentals and Applications”, 2nd Edition Pearson Education 2007
3. H P Hsu, Schaum Outline Series - “Analog and Digital Communications” TMH 2006
4. Couch.L., "Modern Communication Systems", Pearson, 2001.

PTEC6403 ELECTROMAGNETIC FIELDS L T P C

3 1 0 4

OBJECTIVES:

- To impart knowledge on the basics of static electric and magnetic field and the associated laws.
- To give insight into the propagation of EM waves and also to introduce the methods in computational electromagnetics.
- To make students have depth understanding of antennas, electronic devices, Waveguides is possible.

UNIT I STATIC ELECTRIC FIELD 9

Vector Algebra, Coordinate Systems, Vector differential operator, Gradient, Divergence, Curl, Divergence theorem, Stokes theorem, Coulombs law, Electric field intensity, Point, Line, Surface and Volume charge distributions, Electric flux density, Gauss law and its applications, Gauss divergence theorem, Absolute Electric potential, Potential difference, Calculation of potential differences for different configurations. Electric dipole, Electrostatic Energy and Energy density.

UNIT II CONDUCTORS AND DIELECTRICS 9

Conductors and dielectrics in Static Electric Field, Current and current density, Continuity equation, Polarization, Boundary conditions, Method of images, Resistance of a conductor, Capacitance, Parallel plate, Coaxial and Spherical capacitors, Boundary conditions for perfect dielectric materials, Poisson's equation, Laplace's equation, Solution of Laplace equation, Application of Poisson's and Laplace's equations.

UNIT III STATIC MAGNETIC FIELDS 9

Biot -Savart Law, Magnetic field Intensity, Estimation of Magnetic field Intensity for straight and circular conductors, Ampere's Circuital Law, Point form of Ampere's Circuital Law, Stokes theorem, Magnetic flux and magnetic flux density, The Scalar and Vector Magnetic potentials, Derivation of Steady magnetic field Laws.

UNIT IV MAGNETIC FORCES AND MATERIALS 9

Force on a moving charge, Force on a differential current element, Force between current elements, Force and torque on a closed circuit, The nature of magnetic materials, Magnetization and permeability, Magnetic boundary conditions involving magnetic fields, The magnetic circuit, Potential energy and forces on magnetic materials, Inductance, Basic expressions for self and mutual inductances, Inductance evaluation for solenoid, toroid, coaxial cables and transmission lines, Energy stored in Magnetic fields.

UNIT V TIME VARYING FIELDS AND MAXWELL'S EQUATIONS 9

Fundamental relations for Electrostatic and Magnetostatic fields, Faraday's law for Electromagnetic induction, Transformers, Motional Electromotive forces, Differential form of Maxwell's equations, Integral form of Maxwell's equations, Potential functions, Electromagnetic boundary conditions, Wave equations and their solutions, Poynting's theorem, Time harmonic fields, Electromagnetic Spectrum.

TOTAL (L:45+T:15): 60 PERIODS

OUTCOMES:

Upon completion of the course, the students would be able to

- Analyze field potentials due to static changes and static magnetic fields.
- Explain how materials affect electric and magnetic fields.
- Analyze the relation between the fields under time varying situations.
- Discuss the principles of propagation of uniform plane waves.

TEXT BOOKS:

1. William H Hayt and Jr John A Buck, "Engineering Electromagnetics", Tata Mc Graw-Hill Publishing Company Ltd, New Delhi, 2008
2. Sadiku MH, "Principles of Electromagnetics", Oxford University Press Inc, New Delhi, 2009

REFERENCES:

1. David K Cheng, "Field and Wave Electromagnetics", Pearson Education Inc, Delhi, 2004
2. John D Kraus and Daniel A Fleisch, "Electromagnetics with Applications", Mc Graw Hill Book Co, 2005
3. Karl E Longman and Sava V Savov, "Fundamentals of Electromagnetics", Prentice Hall of India, New Delhi, 2006
4. Ashutosh Pramanic, "Electromagnetism", Prentice Hall of India , New Delhi, 2006

PTEC6404 LINEAR INTEGRATED CIRCUITS L T P C
3 0 0 3

OBJECTIVES:

- To introduce the basic building blocks of linear integrated circuits.
- To learn the linear and non-linear applications of operational amplifiers.
- To introduce the theory and applications of analog multipliers and PLL.
- To learn the theory of ADC and DAC.
- To introduce the concepts of waveform generation and introduce some special function ICs.

UNIT I BASICS OF OPERATIONAL AMPLIFIERS 9

Current mirror and current sources, Current sources as active loads, Voltage sources, Voltage References, BJT Differential amplifier with active loads, Basic information about op-amps – Ideal Operational Amplifier - General operational amplifier stages -and internal circuit diagrams of IC 741, DC and AC performance characteristics, slew rate, Open and closed loop configurations.

UNIT II APPLICATIONS OF OPERATIONAL AMPLIFIERS 9

Sign Changer, Scale Changer, Phase Shift Circuits, Voltage Follower, V-to-I and I-to-V converters, adder, subtractor, Instrumentation amplifier, Integrator, Differentiator, Logarithmic amplifier, Antilogarithmic amplifier, Comparators, Schmitt trigger, Precision rectifier, peak detector, clipper and clamper, Low-pass, high-pass and band-pass Butterworth filters.

UNIT III ANALOG MULTIPLIER AND PLL 9

Analog Multiplier using Emitter Coupled Transistor Pair - Gilbert Multiplier cell – Variable transconductance technique, analog multiplier ICs and their applications, Operation of the basic PLL, Closed loop analysis, Voltage controlled oscillator, Monolithic PLL IC 565, application of PLL for AM detection, FM detection, FSK modulation and demodulation and Frequency synthesizing.

UNIT IV ANALOG TO DIGITAL AND DIGITAL TO ANALOG CONVERTERS 9

Analog and Digital Data Conversions, D/A converter – specifications - weighted resistor type, R-2R Ladder type, Voltage Mode and Current-Mode *R* □ *2R* Ladder types - switches for D/A converters, high speed sample-and-hold circuits, A/D Converters – specifications - Flash type - Successive Approximation type - Single Slope type – Dual Slope type - A/D Converter using Voltage-to-Time Conversion - Over-sampling A/D Converters.

UNIT V WAVEFORM GENERATORS AND SPECIAL FUNCTION ICS 9

Sine-wave generators, Multivibrators and Triangular wave generator, Saw-tooth wave generator, ICL8038 function generator, Timer IC 555, IC Voltage regulators – Three terminal fixed and adjustable voltage regulators - IC 723 general purpose regulator - Monolithic switching regulator, Switched capacitor filter IC MF10, Frequency to Voltage and Voltage to Frequency converters, Audio Power amplifier, Video Amplifier, Isolation Amplifier, Opto-couplers and fibre optic IC.

TOTAL: 45 PERIODS

OUTCOMES:

Upon Completion of the course, the students will be able to:

- Design linear and non linear applications of op – amps.
- Design applications using analog multiplier and PLL.
- Design ADC and DAC using op – amps.
- Generate waveforms using op – amp circuits.
- Analyze special function ICs.

TEXT BOOKS:

1. D.Roy Choudhry, Shail Jain, “Linear Integrated Circuits”, New Age International Pvt. Ltd.,2000.
2. Sergio Franco, “Design with Operational Amplifiers and Analog Integrated Circuits”, 3rd Edition, Tata Mc Graw-Hill, 2007.

REFERENCES:

1. Ramakant A. Gayakwad, “OP-AMP and Linear ICs”, 4th Edition, Prentice Hall / Pearson Education, 2001.
2. Robert F.Coughlin, Frederick F.Driscoll, “Operational Amplifiers and Linear Integrated Circuits”, Sixth Edition, PHI, 2001.
3. B.S.Sonde, “System design using Integrated Circuits” , 2nd Edition, New Age Pub, 2001
4. Gray and Meyer, “Analysis and Design of Analog Integrated Circuits”, Wiley International, 2005.
5. Michael Jacob, “Applications and Design with Analog Integrated Circuits”, Prentice Hall of India, 1996.
6. William D.Stanley, “Operational Amplifiers with Linear Integrated Circuits”, Pearson Education, 2004.
7. S.Salivahanan & V.S. Kanchana Bhaskaran, “Linear Integrated Circuits”, TMH, 2008.

PTEC6412 LINEAR INTEGRATED CIRCUITS LABORATORY

L	T	P	C
0	0	3	2

OBJECTIVES:

- To expose the students to linear and integrated circuits
- To understand the basics of linear integrated circuits and available ICs
- To understand characteristics of operational amplifier.
- To apply operational amplifiers in linear and nonlinear applications.
- To acquire the basic knowledge of special function IC.
- To use SPICE software for circuit design

LIST OF EXPERIMENTS:

DESIGN AND TESTING OF

1. Inverting, Non inverting and Differential amplifiers.
2. Integrator and Differentiator.
3. Instrumentation amplifier
4. Active low-pass, High-pass and band-pass filters.
5. Astable & Monostable multivibrators and Schmitt Trigger using op-amp.
6. Phase shift and Wien bridge oscillators using op-amp.
7. Astable and monostable multivibrators using NE555 Timer.
8. PLL characteristics and its use as Frequency Multiplier.
9. DC power supply using LM317 and LM723.
10. Study of SMPS.

SIMULATION USING SPICE

1. Simulation of Experiments 3, 4, 5, 6 and 7.
2. D/A and A/D converters (Successive approximation)
3. Analog multiplier
4. CMOS Inverter, NAND and NOR

TOTAL: 45 PERIODS

OUTCOMES:

At the end of the course, the student should be able to:

- Design oscillators and amplifiers using operational amplifiers.
- Design filters using Opamp and perform experiment on frequency response.
- Analyse the working of PLL and use PLL as frequency multiplier.
- Design DC power supply using ICs.
- Analyse the performance of oscillators and multivibrators using SPICE

LAB EQUIPMENTS FOR A BATCH OF 30 STUDENTS (2 students per Experiment)

CRO (Min 30MHz) – 15 Nos.
Signal Generator /Function Generators (2 MHz) – 15 Nos
Dual Regulated Power Supplies (0 – 30V) – 15 Nos.
Digital Multimeter – 15 Nos
IC tester - 5 Nos
Standalone desktops PC – 15 Nos.
SPICE Circuit Simulation Software: (any public domain or commercial software)
Components and Accessories: - 50 Nos

Transistors, Resistors, Capacitors, diodes, Zener diodes, Bread Boards, Transformers, wires, Power transistors, Potentiometer, A/D and D/A convertors, LEDs

Note: Op-Amps uA741, LM 301, LM311, LM 324, LM317, LM723, 7805, 7812, 2N3524, 2N3525, 2N3391, AD 633, LM 555, LM 565 may be used.

PTEC6501	DIGITAL COMMUNICATION	L	T	P	C
		3	0	0	3

OBJECTIVES:

- To know the principles of sampling & quantization
- To study the various waveform coding schemes
- To learn the various baseband transmission schemes
- To understand the various Band pass signaling schemes
- To know the fundamentals of channel coding

UNIT I SAMPLING & QUANTIZATION 9

Low pass sampling – Aliasing- Signal Reconstruction-Quantization - Uniform & non-uniform quantization - quantization noise - Logarithmic Companding of speech signal- PCM - TDM

UNIT II WAVEFORM CODING 9

Prediction filtering and DPCM - Delta Modulation - ADPCM & ADM principles-Linear Predictive Coding

UNIT III BASEBAND TRANSMISSION 9

Properties of Line codes- Power Spectral Density of Unipolar / Polar RZ & NRZ – Bipolar NRZ - Manchester- ISI – Nyquist criterion for distortionless transmission – Pulse shaping – Correlative coding - Mary schemes – Eye pattern - Equalization

UNIT IV DIGITAL MODULATION SCHEME 9

Geometric Representation of signals - Generation, detection, PSD & BER of Coherent BPSK, BFSK & QPSK - QAM - Carrier Synchronization - structure of Non-coherent Receivers - Principle of DPSK.

UNIT V ERROR CONTROL CODING 9

Channel coding theorem - Linear Block codes - Hamming codes - Cyclic codes - Convolutional codes - Vitterbi Decoder

TOTAL: 45 PERIODS

OUTCOMES:

Upon completion of the course, students will be able to

- design PCM systems
- design and implement base band transmission schemes
- design and implement band pass signaling schemes
- analyze the spectral characteristics of band pass signaling schemes and their noise performance
- design error control coding schemes

TEXT BOOK:

1. S. Haykin, “Digital Communications”, John Wiley, 2005

REFERENCES:

1. B. Sklar, “Digital Communication Fundamentals and Applications”, 2nd Edition, Pearson Education, 2009
2. B.P.Lathi, “Modern Digital and Analog Communication Systems” 3rd Edition, Oxford University Press 2007.
3. H P Hsu, Schaum Outline Series - “Analog and Digital Communications”, TMH 2006
4. J.G Proakis, “Digital Communication”, 4th Edition, Tata Mc Graw Hill Company, 2001.

PTEC6502 PRINCIPLES OF DIGITAL SIGNAL PROCESSING L T P C

3 1 0 4

OBJECTIVES:

- To learn discrete Fourier transform and its properties
- To know the characteristics of IIR and FIR filters learn the design of infinite and finite impulse response filters for filtering undesired signals
- To understand Finite word length effects
- To study the concept of Multirate and adaptive filters

UNIT I DISCRETE FOURIER TRANSFORM 9

Discrete Signals and Systems- A Review – Introduction to DFT – Properties of DFT – Circular Convolution - Filtering methods based on DFT – FFT Algorithms –Decimation in time Algorithms, Decimation in frequency Algorithms – Use of FFT in Linear Filtering.

UNIT II IIR FILTER DESIGN 9

Structures of IIR – Analog filter design – Discrete time IIR filter from analog filter – IIR filter design by Impulse Invariance, Bilinear transformation, Approximation of derivatives – (LPF, HPF, BPF, BRF) filter design using frequency translation.

UNIT III FIR FILTER DESIGN 9

Structures of FIR – Linear phase FIR filter – Fourier Series - Filter design using windowing techniques (Rectangular Window, Hamming Window, Hanning Window), Frequency sampling techniques – Finite word length effects in digital Filters: Errors, Limit Cycle, Noise Power Spectrum.

UNIT IV FINITE WORDLENGTH EFFECTS 9

Fixed point and floating point number representations – ADC –Quantization- Truncation and Rounding errors - Quantization noise – coefficient quantization error – Product quantization error - Overflow error – Roundoff noise power - limit cycle oscillations due to product round off and overflow errors – Principle of scaling

UNIT V DSP APPLICATIONS 9

Multirate signal processing: Decimation, Interpolation, Sampling rate conversion by a rational factor – Adaptive Filters: Introduction, Applications of adaptive filtering to equalization.

TOTAL (L:45+T:15): 60 PERIODS

OUTCOMES:

Upon completion of the course, students will be able to

- apply DFT for the analysis of digital signals & systems
- design IIR and FIR filters
- characterize finite Word length effect on filters
- design the Multirate Filters
- apply Adaptive Filters to equalization

TEXT BOOK:

1. John G. Proakis & Dimitris G.Manolakis, "Digital Signal Processing – Principles, Algorithms & Applications", Fourth Edition, Pearson Education / Prentice Hall, 2007.

REFERENCES:

1. Emmanuel C..Ifeachor, & Barrie.W.Jervis, "Digital Signal Processing", Second Edition, Pearson Education / Prentice Hall, 2002.
2. Sanjit K. Mitra, "Digital Signal Processing – A Computer Based Approach", Tata Mc Graw Hill, 2007.
3. A.V.Oppenheim, R.W. Schafer and J.R. Buck, "Discrete-Time Signal Processing", 8th Indian Reprint, Pearson, 2004.
4. Andreas Antoniou, "Digital Signal Processing", Tata Mc Graw Hill, 2006.

PTEC6503 TRANSMISSION LINES AND WAVE GUIDES

L	T	P	C
3	1	0	4

OBJECTIVES:

- To introduce the various types of transmission lines and to discuss the losses associated.
- To give thorough understanding about impedance transformation and matching.
- To use the Smith chart in problem solving.
- To impart knowledge on filter theories and waveguide theories

UNIT I TIME VARRYING FIELDS AND MAXWELL'S EQUTIONS 9

Motional Electromotive Force, General Expression for motional EMF, Faraday's Law of Induction, Displacement current, Maxwell's equation in the point or differential form, Maxwell's equations in Integral form, Maxwell's equations from Gauss's Law, Maxwell's equations and Boundary conditions, Poynting's theorem, Time harmonic (sinusoidal) fields, Maxwell's equations in phasor form.

UNIT II TRANSMISSION LINES 9

Need for Transmission Lines, Types of Transmission lines, Characterization in terms of primary and secondary constants, Characteristic impedance, General wave equation, Loss less propagation, Propagation constant, Wave reflection at discontinuities, Voltage standing wave ratio, Transmission line of finite length, The Smith Chart, Smith Chart calculations for lossy lines, Impedance matching by Quarter wave transformer, Single and double stub matching.

UNIT III THE UNIFORM PLANE WAVE 9

Wave propagation in free space, Wave propagation in dielectrics, Forward and Backward Travelling Wave, Poynting Theorem and Wave Power, Energy of the Radiated wave, Propagation in good conductors and good dielectrics, Skin effect, Wave polarization, Linearly, Elliptically and Circularly polarized waves,

UNIT IV TRANSMISSION AND REFLECTION OF PLANE WAVES AT BOUNDARIES 9

Normal incidence of Uniform Plane waves: Conductor-Conductor interface, Dielectric-Dielectric interface, Dielectric-perfect Conductor interface, Dielectric-Conductor interface. Oblique incidence on a plane boundary for perpendicular polarization, Dielectric-Dielectric interface, Dielectric-Conductor interface.

UNIT V WAVE GUIDES AND CAVITY RESONATORS 9

General Wave behaviours along uniform Guiding structures, Transverse Electromagnetic waves, Transverse Magnetic waves, Transverse Electric waves, TM and TE waves between parallel plates, TM and TE waves in Rectangular wave guides, Bessel's differential equation and Bessel function, TM and TE waves in Circular wave guides, Rectangular and circular cavity Resonators.

TOTAL (L:45+T:15): 60 PERIODS .

OUTCOMES:

Upon completion of the course, students will be able to:

- Discuss the propagation of signals through transmission lines.
- Analyze signal propagation at Radio frequencies.
- Explain radio propagation in guided systems.
- Utilize cavity resonators.

TEXT BOOK:

1. John D Ryder, "Networks lines and fields", Prentice Hall of India, New Delhi, 2005

REFERENCES:

1. William H Hayt and Jr John A Buck, "Engineering Electromagnetics" Tata Mc Graw-Hill Publishing Company Ltd, New Delhi, 2008
2. David K Cheng, "Field and Wave Electromagnetics", Pearson Education Inc, Delhi, 2004
3. John D Kraus and Daniel A Fleisch, "Electromagnetics with Applications", Mc Graw Hill Book Co, 2005
4. GSN Raju, "Electromagnetic Field Theory and Transmission Lines", Pearson Education, 2005
5. Bhag Singh Guru and HR Hiziroglu, "Electromagnetic Field Theory Fundamentals", Vikas Publishing House, New Delhi, 2001.
6. N. Narayana Rao, " Elements of Engineering Electromagnetics" 6th edition Prentice Hall,2004

PTEC6504 MICROPROCESSOR AND MICROCONTROLLER L T P C
3 0 0 3

OBJECTIVES:

The student should be made to:

- Study the Architecture of 8086 microprocessor.
- Learn the design aspects of I/O and Memory Interfacing circuits.
- Study about communication and bus interfacing.
- Study the Architecture of 8051 microcontroller.

UNIT I THE 8086 MICROPROCESSOR 9

Introduction to 8086 – Microprocessor architecture – Addressing modes - Instruction set and assembler directives – Assembly language programming – Modular Programming - Linking and Relocation - Stacks - Procedures – Macros – Interrupts and interrupt service routines – Byte and String Manipulation.

UNIT II 8086 SYSTEM BUS STRUCTURE 9

8086 signals – Basic configurations – System bus timing –System design using 8086 – IO programming – Introduction to Multiprogramming – System Bus Structure - Multiprocessor configurations – Coprocessor, Closely coupled and loosely Coupled configurations – Introduction to advanced processors.

UNIT III I/O INTERFACING 9

Memory Interfacing and I/O interfacing - Parallel communication interface – Serial communication interface – D/A and A/D Interface - Timer – Keyboard /display controller – Interrupt controller – DMA controller – Programming and applications Case studies: Traffic Light control, LED display , LCD display, Keyboard display interface and Alarm Controller.

UNIT IV MICROCONTROLLER 9

Architecture of 8051 – Special Function Registers(SFRs) - I/O Pins Ports and Circuits - Instruction set - Addressing modes - Assembly language programming.

UNIT V INTERFACING MICROCONTROLLER 9

Programming 8051 Timers - Serial Port Programming - Interrupts Programming – LCD & Keyboard Interfacing - ADC, DAC & Sensor Interfacing - External Memory Interface- Stepper Motor and Waveform generation.

TOTAL: 45 PERIODS

OUTCOMES:

At the end of the course, the student should be able to:

- Design and implement programs on 8086 microprocessor.
- Design I/O circuits.
- Design Memory Interfacing circuits.
- Design and implement 8051 microcontroller based systems.

TEXT BOOKS:

1. Yu-Cheng Liu, Glenn A.Gibson, “Microcomputer Systems: The 8086 / 8088 Family - Architecture, Programming and Design”, Second Edition, Prentice Hall of India, 2007.
2. Mohamed Ali Mazidi, Janice Gillispie Mazidi, Rolin McKinlay, “The 8051 Microcontroller and Embedded Systems: Using Assembly and C”, Second Edition, Pearson education, 2011.

REFERENCE:

1. Doughlas V.Hall, “Microprocessors and Interfacing, Programming and Hardware”,TMH,2012

PTEC6513 MICROPROCESSOR AND MICROCONTROLLER LABORATORY L T P C
0 0 3 2

OBJECTIVES:

The student should be made to:

- Introduce ALP concepts and features
- Write ALP for arithmetic and logical operations in 8086 and 8051
- Differentiate Serial and Parallel Interface
- Interface different I/Os with Microprocessors
- Be familiar with MASM

LIST OF EXPERIMENTS:

8086 Programs using kits and MASM

1. Basic arithmetic and Logical operations
2. Move a data block without overlap
3. Code conversion, decimal arithmetic and Matrix operations.
4. Floating point operations, string manipulations, sorting and searching
5. Password checking, Print RAM size and system date
6. Counters and Time Delay

Peripherals and Interfacing Experiments

7. Traffic light control
8. Stepper motor control
9. Digital clock
10. Key board and Display
11. Printer status
12. Serial interface and Parallel interface
13. A/D and D/A interface and Waveform Generation

8051 Experiments using kits and MASM

14. Basic arithmetic and Logical operations
15. Square and Cube program, Find 2's complement of a number
16. Unpacked BCD to ASCII

TOTAL: 45 PERIODS

OUTCOMES:

At the end of the course, the student should be able to:

- Write ALP Programmes for fixed and Floating Point and Arithmetic
- Interface different I/Os with processor
- Generate waveforms using Microprocessors
- Execute Programs in 8051
- Explain the difference between simulator and Emulator

LAB EQUIPMENTS FOR A BATCH OF 30 STUDENTS:

HARDWARE:

8086 development kits	- 30 nos
Interfacing Units	- Each 10 nos
Microcontroller	- 30 nos

SOFTWARE:

Intel Desktop Systems with MASM - 30 nos
8086 Assembler
8051 Cross Assembler

PTMG6851 **PRINCIPLES OF MANAGEMENT** **L T P C**
3 0 0 3

OBJECTIVE:

- To enable the students to study the evolution of Management, to study the functions and principles of management and to learn the application of the principles in an organization .

UNIT I INTRODUCTION TO MANAGEMENT AND ORGANIZATIONS 9

Definition of Management – Science or Art – Manager Vs Entrepreneur - types of managers - managerial roles and skills – Evolution of Management – Scientific, human relations , system and contingency approaches – Types of Business organization - Sole proprietorship, partnership, company-public and private sector enterprises - Organization culture and Environment – Current trends and issues in Management.

UNIT II PLANNING 9

Nature and purpose of planning – planning process – types of planning – objectives – setting objectives – policies – Planning premises – Strategic Management – Planning Tools and Techniques – Decision making steps and process.

UNIT III ORGANISING 9

Nature and purpose – Formal and informal organization – organization chart – organization structure – types – Line and staff authority – departmentalization – delegation of authority – centralization and decentralization – Job Design - Human Resource Management – HR Planning, Recruitment, selection, Training and Development, Performance Management , Career planning and management.

UNIT IV DIRECTING 9

Foundations of individual and group behaviour – motivation – motivation theories – motivational techniques – job satisfaction – job enrichment – leadership – types and theories of leadership – communication – process of communication – barrier in communication – effective communication – communication and IT.

UNIT V CONTROLLING 9

System and process of controlling – budgetary and non-budgetary control techniques – use of computers and IT in Management control – Productivity problems and management – control and performance – direct and preventive control – reporting.

TOTAL: 45 PERIODS

OUTCOMES :

- Upon completion of the course, students will be able to have clear understanding of managerial functions like planning, organizing, staffing, leading & controlling and have same basic knowledge on international aspect of management

TEXT BOOKS:

1. Stephen P. Robbins & Mary Coulter, “Management”, 10th Edition, Prentice Hall (India) Pvt. Ltd., 2009.
2. JAF Stoner, Freeman R.E and Daniel R Gilbert “Management”, 6th Edition, Pearson Education, 2004.

REFERENCES:

1. Stephen A. Robbins & David A. Decenzo & Mary Coulter, “Fundamentals of Management” 7th Edition, Pearson Education, 2011.
2. Robert Kreitner & Mamata Mohapatra, “Management”, Biztantra, 2008.
3. Harold Koontz & Heinz Weihrich “Essentials of management” Tata Mc Graw Hill, 1998.
4. Tripathy PC & Reddy PN, “Principles of Management”, Tata McGraw Hill, 1999.

PTCS6303 COMPUTER ARCHITECTURE L T P C
3 0 0 3

OBJECTIVES:

- To make students understand the basic structure and operation of digital computer.
- To understand the hardware-software interface.
- To familiarize the students with arithmetic and logic unit and implementation of fixed point and floating-point arithmetic operations.
- To expose the students to the concept of pipelining.
- To familiarize the students with hierarchical memory system including cache memories and virtual memory.
- To expose the students with different ways of communicating with I/O devices and standard I/O interfaces.

UNIT I OVERVIEW & INSTRUCTIONS 9

Eight ideas – Components of a computer system – Technology – Performance – Power wall – Uniprocessors to multiprocessors; Instructions – operations and operands – representing instructions – Logical operations – control operations – Addressing and addressing modes.

UNIT II ARITHMETIC OPERATIONS 7

ALU - Addition and subtraction – Multiplication – Division – Floating Point operations – Subword parallelism.

UNIT III PROCESSOR AND CONTROL UNIT 11

Basic MIPS implementation – Building datapath – Control Implementation scheme – Pipelining – Pipelined datapath and control – Handling Data hazards & Control hazards – Exceptions.

UNIT IV PARALLELISM 9

Instruction-level-parallelism – Parallel processing challenges – Flynn's classification – Hardware multithreading – Multicore processors

UNIT V MEMORY AND I/O SYSTEMS 9

Memory hierarchy - Memory technologies – Cache basics – Measuring and improving cache performance - Virtual memory, TLBs - Input/output system, programmed I/O, DMA and interrupts, I/O processors.

TOTAL: 45 PERIODS

OUTCOMES:

At the end of the course, the student should be able to:

- Design arithmetic and logic unit.
- Design and anlayse pipelined control units
- Evaluate performance of memory systems.
- Understand parallel processing architectures.

TEXT BOOK:

1. David A. Patterson and John L. Hennessey, “Computer Organization and Design’, Fifth edition, Morgan Kauffman / Elsevier, 2014.

REFERENCES:

1. V.Carl Hamacher, Zvonko G. Varanesic and Safat G. Zaky, “Computer Organisation“, VI edition, Mc Graw-Hill Inc, 2012.
2. William Stallings “Computer Organization and Architecture”, Seventh Edition , Pearson Education, 2006.

3. Vincent P. Heuring, Harry F. Jordan, “Computer System Architecture”, Second Edition, Pearson Education, 2005.
4. Govindarajalu, “Computer Architecture and Organization, Design Principles and Applications", first edition, Tata Mc Graw Hill, New Delhi, 2005.
5. John P. Hayes, “Computer Architecture and Organization”, Third Edition, Tata Mc Graw Hill, 1998.
6. http://nptel.ac.in/.

PTEC6601 VLSI DESIGN L T P C
3 0 0 3

OBJECTIVES:

- In this course, the MOS circuit realization of the various building blocks that is common to any microprocessor or digital VLSI circuit is studied.
- Architectural choices and performance tradeoffs involved in designing and realizing the circuits in CMOS technology are discussed.
- The main focus in this course is on the transistor circuit level design and realization for digital operation and the issues involved as well as the topics covered are quite distinct from those encountered in courses on CMOS Analog IC design.

UNIT I MOS TRANSISTOR PRINCIPLE 9

NMOS and PMOS transistors, Process parameters for MOS and CMOS, Electrical properties of CMOS circuits and device modeling, Scaling principles and fundamental limits, CMOS inverter scaling, propagation delays, Stick diagram, Layout diagrams

UNIT II COMBINATIONAL LOGIC CIRCUITS 9

Examples of Combinational Logic Design, Elmore’s constant, Pass transistor Logic, Transmission gates, static and dynamic CMOS design, Power dissipation – Low power design principles

UNIT III SEQUENTIAL LOGIC CIRCUITS 9

Static and Dynamic Latches and Registers, Timing issues, pipelines, clock strategies, Memory architecture and memory control circuits, Low power memory circuits, Synchronous and Asynchronous design

UNIT IV DESIGNING ARITHMETIC BUILDING BLOCKS 9

Data path circuits, Architectures for ripple carry adders, carry look ahead adders, High speed adders, accumulators, Multipliers, dividers, Barrel shifters, speed and area tradeoff

UNIT V IMPLEMENTATION STRATEGIES 9

Full custom and Semi custom design, Standard cell design and cell libraries, FPGA building block architectures, FPGA interconnect routing procedures.

TOTAL: 45 PERIODS

OUTCOMES:

Upon completion of the course, students should

- Explain the basic CMOS circuits and the CMOS process technology.
- Discuss the techniques of chip design using programmable devices.
- Model the digital system using Hardware Description Language.

TEXTBOOKS:

1. Jan Rabaey, Anantha Chandrakasan, B.Nikolic, “Digital Integrated Circuits: A Design Perspective”, Second Edition, Prentice Hall of India, 2003.
2. M.J. Smith, “Application Specific Integrated Circuits”, Addisson Wesley, 1997

REFERENCES:

1. N.Weste, K.Eshraghian, “Principles of CMOS VLSI Design”, Second Edition, Addision Wesley 1993
2. R.Jacob Baker, Harry W.LI., David E.Boyee, “CMOS Circuit Design, Layout and Simulation”, Prentice Hall of India 2005
3. A.Pucknell, Kamran Eshraghian, “BASIC VLSI Design”, Third Edition, Prentice Hall of India, 2007.

PTEC6602 ANTENNA AND WAVE PROPAGATION L T P C
3 0 0 3

OBJECTIVES:

- To give insight of the radiation phenomena.
- To give a thorough understanding of the radiation characteristics of different types of antennas
- To create awareness about the different types of propagation of radio waves at different frequencies

UNIT I FUNDAMENTALS OF RADIATION 9

Definition of antenna parameters – Gain, Directivity, Effective aperture, Radiation Resistance, Band width, Beam width, Input Impedance. Matching – Baluns, Polarization mismatch, Antenna noise temperature, Radiation from oscillating dipole, Half wave dipole. Folded dipole, Yagi array.

UNIT II APERTURE AND SLOT ANTENNAS 9

Radiation from rectangular apertures, Uniform and Tapered aperture, Horn antenna , Reflector antenna , Aperture blockage , Feeding structures , Slot antennas ,Microstrip antennas – Radiation mechanism – Application ,Numerical tool for antenna analysis

UNIT III ANTENNA ARRAYS 9

N element linear array, Pattern multiplication, Broadside and End fire array – Concept of Phased arrays, Adaptive array, Basic principle of antenna Synthesis-Binomial array

UNIT IV SPECIAL ANTENNAS 9

Principle of frequency independent antennas –Spiral antenna, Helical antenna, Log periodic. Modern antennas- Reconfigurable antenna, Active antenna, Dielectric antennas, Electronic band gap structure and applications, Antenna Measurements-Test Ranges, Measurement of Gain, Radiation pattern, Polarization, VSWR

UNIT V PROPAGATION OF RADIO WAVES 9

Modes of propagation , Structure of atmosphere , Ground wave propagation , Tropospheric propagation , Duct propagation, Troposcatter propagation , Flat earth and Curved earth concept Sky wave propagation – Virtual height, critical frequency , Maximum usable frequency – Skip distance, Fading , Multi hop propagation

TOTAL: 45 PERIODS

OUTCOMES:

Upon completion of the course, students will be able to

- Explain the various types of antennas and wave propagation.
- Write about the radiation from a current element.
- Analyze the antenna arrays, aperture antennas and special antennas such as frequency independent and broad band

TEXT BOOK:

1. John D Kraus," Antennas for all Applications", 3rd Edition, Mc Graw Hill, 2005.

REFERENCES:

1. Edward C.Jordan and Keith G.Balmain" Electromagnetic Waves and Radiating Systems" Prentice Hall of India, 2006
2. R.E.Collin,"Antennas and Radiowave Propagation", Mc Graw Hill 1985.
3. Constantine.A.Balanis "Antenna Theory Analysis and Design", Wiley Student Edition, 2006.
4. Rajeswari Chatterjee, "Antenna Theory and Practice" Revised Second Edition New Age International Publishers, 2006.
5. S. Drabowitch, "Modern Antennas" Second Edition, Springer Publications, 2007.
6. Robert S.Elliott "Antenna Theory and Design" Wiley Student Edition, 2006.
7. H.Sizun "Radio Wave Propagation for Telecommunication Applications", First Indian Reprint, Springer Publications, 2007.

PTEC6612	VLSI DESIGN LABORATORY	L	T	P	C
		0	0	3	2

OBJECTIVES

- To learn Hardware Descriptive Language(Verilog/VHDL)
- To learn the fundamental principles of VLSI circuit design in digital and analog domain
- To familiarise fusing of logical modules on FPGAs
- To provide hands on design experience with professional design (EDA) platforms.

LIST OF EXPERIMENTS

FPGA BASED EXPERIMENTS.

1. HDL based design entry and simulation of simple counters, state machines, adders (min 8 bit) and multipliers (4 bit min).
2. Synthesis, P&R and post P&R simulation of the components simulated in (I) above. Critical paths and static timing analysis results to be identified. Identify and verify possible conditions under which the blocks will fail to work correctly.
3. Hardware fusing and testing of each of the blocks simulated in (I). Use of either chipscope feature (Xilinx) or the signal tap feature (Altera) is a must. Invoke the PLL and demonstrate the use of the PLL module for clock generation in FPGAs.

IC DESIGN EXPERIMENTS: (BASED ON CADENCE / MENTOR GRAPHICS / EQUIVALENT)

4. Design and simulation of a simple 5 transistor differential amplifier. Measure gain, ICMR, and CMRR
5. Layout generation, parasitic extraction and resimulation of the circuit designed in (I)
6. Synthesis and Standard cell based design of an circuits simulated in 1(I) above. Identification of critical paths, power consumption.
7. For expt (c) above, P&R, power and clock routing, and post P&R simulation.
8. Analysis of results of static timing analysis.

TOTAL: 45 PERIODS

OUTCOMES:

At the end of the course, the student should be able to

- Write HDL code for basic as well as advanced digital integrated circuits.
- Import the logic modules into FPGA Boards.
- Synthesize, Place and Route the digital IPs.
- Design, Simulate and Extract the layouts of Analog IC Blocks using EDA tools.

LAB EQUIPMENTS FOR A BATCH OF 30 STUDENSTS:

Xilinx or Altera FPGA	10 nos
Xilinx software	
Cadence/MAGMA/Tanner or equivalent software package	10 User License
PCs	10 No.s

PTEC6801 WIRELESS COMMUNICATION L T P C
3 0 0 3

OBJECTIVES:

The student should be made to:

- Know the characteristic of wireless channel
- Learn the various cellular architectures
- Understand the concepts behind various digital signaling schemes for fading channels
- Be familiar the various multipath mitigation techniques
- Understand the various multiple antenna systems

UNIT I WIRELESS CHANNELS 9

Large scale path loss – Path loss models: Free Space and Two-Ray models -Link Budget design – Small scale fading- Parameters of mobile multipath channels – Time dispersion parameters- Coherence bandwidth – Doppler spread & Coherence time, Fading due to Multipath time delay spread – flat fading – frequency selective fading – Fading due to Doppler spread – fast fading – slow fading.

UNIT II CELLULAR ARCHITECTURE 9

Multiple Access techniques - FDMA, TDMA, CDMA – Capacity calculations–Cellular concept- Frequency reuse - channel assignment- hand off- interference & system capacity- trunking & grade of service – Coverage and capacity improvement.

UNIT III DIGITAL SIGNALING FOR FADING CHANNELS 9

Structure of a wireless communication link, Principles of Offset-QPSK, p/4-DQPSK, Minimum Shift Keying, Gaussian Minimum Shift Keying, Error performance in fading channels, OFDM principle – Cyclic prefix, Windowing, PAPR.

UNIT IV MULTIPATH MITIGATION TECHNIQUES 9

Equalisation – Adaptive equalization, Linear and Non-Linear equalization, Zero forcing and LMS Algorithms. Diversity – Micro and Macrodiversity, Diversity combining techniques, Error probability in fading channels with diversity reception, Rake receiver,

UNIT V MULTIPLE ANTENNA TECHNIQUES 9

MIMO systems – spatial multiplexing -System model -Pre-coding - Beam forming - transmitter diversity, receiver diversity- Channel state information-capacity in fading and non-fading channels.

TOTAL : 45 PERIODS

OUTCOMES:

At the end of the course, the student should be able to:

- Characterize wireless channels
- Design and implement various signaling schemes for fading channels
- Design a cellular system
- Compare multipath mitigation techniques and analyze their performance
- Design and implement systems with transmit/receive diversity and MIMO systems and analyze their performance

TEXT BOOKS:

1. Rappaport,T.S., "Wireless communications", Second Edition, Pearson Education, 2010.
2. Andreas.F. Molisch, "Wireless Communications", John Wiley – India, 2006.

REFERENCES:

1. David Tse and Pramod Viswanath, "Fundamentals of Wireless Communication", Cambridge University Press, 2005.
2. Upena Dalal, " Wireless Communication", Oxford University Press, 2009.
3. Van Nee, R. and Ramji Prasad, "OFDM for wireless multimedia communications", Artech House, 2000.

PTEC6702 OPTICAL COMMUNICATION AND NETWORKS L T P C
3 0 0 3

OBJECTIVES:

- To Facilitate the knowledge about optical fiber sources and transmission techniques
- To Enrich the idea of optical fiber networks algorithm such as SONET/SDH and optical CDMA.
- To Explore the trends of optical fiber measurement systems.

UNIT I INTRODUCTION TO OPTICAL FIBERS 9

Evolution of fiber optic system- Element of an Optical Fiber Transmission link-- Total internal reflection-Acceptance angle –Numerical aperture – Skew rays Ray Optics-Optical Fiber Modes and Configurations -Mode theory of Circular Wave guides- Overview of Modes-Key Modal concepts-Linearly Polarized Modes -Single Mode Fibers-Graded Index fiber structure.

UNIT II SIGNAL DEGRADATION OPTICAL FIBERS 9

Attenuation - Absorption losses, Scattering losses, Bending Losses, Core and Cladding losses, Signal Distortion in Optical Wave guides-Information Capacity determination -Group Delay-Material Dispersion, Wave guide Dispersion, Signal distortion in SM fibers-Polarization Mode dispersion, Intermodal dispersion, Pulse Broadening in GI fibers-Mode Coupling -Design Optimization of SM fibers-RI profile and cut-off wavelength.

UNIT III FIBER OPTICAL SOURCES AND COUPLING 9

Direct and indirect Band gap materials-LED structures -Light source materials -Quantum efficiency and LED power, Modulation of a LED, lasers Diodes-Modes and Threshold condition -Rate equations -External Quantum efficiency -Resonant frequencies -Laser Diodes, Temperature effects, Introduction to Quantum laser, Fiber amplifiers- Power Launching and coupling, Lencing schemes, Fiber -to- Fiber joints, Fiber splicing-Signal to Noise ratio , Detector response time.

UNIT IV FIBER OPTIC RECEIVER AND MEASUREMENTS 9

Fundamental receiver operation, Pre amplifiers, Error sources – Receiver Configuration– Probability of Error – Quantum limit.Fiber Attenuation measurements- Dispersion measurements – Fiber Refractive index profile measurements – Fiber cut- off Wave length Measurements – Fiber Numerical Aperture Measurements – Fiber diameter measurements.

UNIT V OPTICAL NETWORKS AND SYSTEM TRANSMISSION 9

Basic Networks – SONET / SDH – Broadcast – and –select WDM Networks –Wavelength Routed Networks – Non linear effects on Network performance –-Link Power budget -Rise time budget-Noise Effects on System Performance-Operational Principles of WDM Performance of WDM + EDFA system – Solutions – Optical CDMA – Ultra High Capacity Networks**.**

TOTAL: 45 PERIODS

OUTCOMES:

Upon completion of the course, students will be able to:

- Discuss the various optical fiber modes, configurations and various signal degradation factors associated with optical fiber.
- Explain the various optical sources and optical detectors and their use in the optical communication system.
- Analyze the digital transmission and its associated parameters on system performance.

TEXT BOOKS:

1. Gerd Keiser, "Optical Fiber Communication" Mc Graw -Hill International, 4th Edition., 2010.
2. John M. Senior , “Optical Fiber Communication”, Second Edition, Pearson Education, 2007.

REFERENCES:

1. Ramaswami, Sivarajan and Sasaki “Optical Networks”, Morgan Kaufmann, 2009.
2. J.Senior, "Optical Communication, Principles and Practice", Prentice Hall of India, 3rd Edition, 2008.
3. J.Gower, "Optical Communication System", Prentice Hall of India, 2001.

PTEC6712 OPTICAL AND MICROWAVE LABORATORY L T P C
0 0 3 2

OBJECTIVES:

The student should be made to:

1. Understand the working principle of optical sources, detector, fibers and microwave components
2. Develop understanding of simple optical communication link.
3. Learn about the characteristics and measurements in optical fiber
4. Know about the behavior of microwave components.
5. Practice microwave measurement procedures

LIST OF EXPERIMENTS

OPTICAL EXPERIMENTS

1. DC Characteristics of LED and PIN Photo diode
2. Mode Characteristics of Fibers
3. Measurement of connector and bending losses

4. Fiber optic Analog and Digital Link- frequency response(analog) and eye diagram (digital)
5. Numerical Aperture determination for Fibers
6. Attenuation Measurement in Fibers

MICROWAVE EXPERIMENTS

1. Reflex klystron or Gunn diode characteristics and basic microwave parameter measurement such as VSWR, frequency, wavelength.
2. Directional Coupler Characteristics.
3. Radiation Pattern of Horn Antenna.
4. S-parameter Measurement of the following microwave components (Isolator, Circulator, E plane Tee, H Plane Tee, Magic Tee)
5. Attenuation and Power Measurement

TOTAL: 45 PERIODS

LIST OF EQUIPMENTS FOR A BATCH OF 30 STUDENTS 3 STUDENTS PER EXPERIMENT:

1. Trainer kit for carrying out LED and PIN diode characteristics, Digital multi meter, optical power meter. – 2 Nos
2. Trainer kit for determining the mode characteristics, losses in optical fiber.- 2 Nos
3. Trainer kit for analyzing Analog and Digital link performance, 2 Mbps PRBS Data source, 10 MHz signal generator, 20 MHz Digital storage Oscilloscope. - 2 Nos
4. Kit for measuring Numerical aperture and Attenuation of fiber - 2 Nos
5. MM/SM Glass and plastic fiber patch chords with ST/SC/E2000 connectors - 2 set
6. LEDs with ST / SC / E2000 receptacles – 650 / 850 nm - 2 set
7. PiN PDs with ST / SC / E2000 receptacles – 650 / 850 nm - 2 set
8. Microwave test Bench at X band to determine Directional coupler characteristics. - 2 Nos
9. Microwave test Bench at X band and Antenna turn table to measure Radiation pattern of Horn antenna, 2 Horn antennas. - 2 Nos
10. Microwave test Bench at X band to determine VSWR for Isolator and Circulator, VSWR meter, Isolator, Circulator, E Plane Tee, H plane Tee. - 2 Nos
11. Microwave test Bench at X band, Variable attenuator, Detector and 20 MHz Digital / Analog Oscilloscope. - 2 Nos

Note: Microwave test bench comprises of Reflex klystron or Gunn diode with power supply, Gunn oscillator, PIN modulator, Isolator, Fixed and Variable Attenuator, frequency meter, Slotted section, Wave guides, detector with mount, Termination, Movable short, Slide screw tuner, Horn antenna, Directional coupler and 20 MHz Digital / Analog Oscilloscope.

OUTCOMES:

At the end of the course, the student should be able to:

1. Analyze the performance of simple optical link.
2. Test microwave and optical components.
3. Analyse the mode characteristics of fiber
4. Analyse the radiation of pattern of antenna.

PTEC6802 **WIRELESS NETWORKS** **L T P C**
3 0 0 3

OBJECTIVES:

- To study about Wireless networks, protocol stack and standards.
- To study about fundamentals of 3G Services, its protocols and applications.
- To study about evolution of 4G Networks, its architecture and applications.

UNIT I WIRELESS LAN **9**

Introduction-WLAN technologies: Infrared, UHF narrowband, spread spectrum -IEEE802.11: System architecture, protocol architecture, physical layer, MAC layer, 802.11b, 802.11a – Hiper LAN: WATM, BRAN, HiperLAN2 – Bluetooth: Architecture, Radio Layer, Baseband layer, Link manager Protocol, security - IEEE802.16-WIMAX: Physical layer, MAC, Spectrum allocation for WIMAX

UNIT II MOBILE NETWORK LAYER **9**

Introduction - Mobile IP: IP packet delivery, Agent discovery, tunneling and encapsulation, IPV6-Network layer in the internet- Mobile IP session initiation protocol - mobile ad-hoc network: Routing, Destination Sequence distance vector, Dynamic source routing

UNIT III MOBILE TRANSPORT LAYER **9**

TCP enhancements for wireless protocols - Traditional TCP: Congestion control, fast retransmit/fast recovery, Implications of mobility - Classical TCP improvements: Indirect TCP, Snooping TCP, Mobile TCP, Time out freezing, Selective retransmission, Transaction oriented TCP - TCP over 3G wireless networks**.**

UNIT IV WIRELESS WIDE AREA NETWORK **9**

Overview of UTMS Terrestrial Radio access network-UMTS Core network Architecture: 3G-MSC, 3G-SGSN, 3G-GGSN, SMS-GMSC/SMS-IWMSC, Firewall, DNS/DHCP-High speed Downlink packet access (HSDPA)- LTE network architecture and protocol.

UNIT V 4G NETWORKS **9**

Introduction – 4G vision – 4G features and challenges - Applications of 4G – 4G Technologies: Multicarrier Modulation, Smart antenna techniques, OFDM-MIMO systems, Adaptive Modulation and coding with time slot scheduler, Cognitive Radio.

TOTAL: 45 PERIODS

OUTCOMES:

Upon completion of the course, the students will be able to

- Conversant with the latest 3G/4G and WiMAX networks and its architecture.
- Design and implement wireless network environment for any application using latest wireless protocols and standards.
- Implement different type of applications for smart phones and mobile devices with latest network strategies.

TEXT BOOKS:

1. Jochen Schiller, ”Mobile Communications”, Second Edition, Pearson Education 2012.(Unit I,II,III)
2. Vijay Garg , “Wireless Communications and networking”, First Edition, Elsevier 2007.(Unit IV,V)

REFERENCES:

1. Erik Dahlman, Stefan Parkvall, Johan Skold and Per Beming, "3G Evolution HSPA and LTE for Mobile Broadband”, Second Edition, Academic Press, 2008.
2. Anurag Kumar, D.Manjunath, Joy kuri, “Wireless Networking”, First Edition, Elsevier 2011.
3. Simon Haykin , Michael Moher, David Koilpillai, “Modern Wireless Communications”, First Edition, Pearson Education 2013

PTEC6811 **PROJECT WORK** **L T P C**
0 0 9 6

OBJECTIVES:

- To develop the ability to solve a specific problem right from its identification and literature review till the successful solution of the same. To train the students in preparing project reports and to face reviews and viva voce examination.

The students in a group of 3 to 4 works on a topic approved by the head of the department under the guidance of a faculty member and prepares a comprehensive project report after completing the work to the satisfaction of the supervisor. The progress of the project is evaluated based on a minimum of three reviews. The review committee may be constituted by the Head of the Department. A project report is required at the end of the semester. The project work is evaluated based on oral presentation and the project report jointly by external and internal examiners constituted by the Head of the Department.

OUTCOMES:

- On Completion of the project work students will be in a position to take up any challenging practical problems and find solution by formulating proper methodology.

TOTAL : 135 PERIODS

PTEC6001 **MEDICAL ELECTRONICS** **L T P C**
3 0 0 3

OBJECTIVES:

- To gain knowledge about the various physiological parameters both electrical and non electrical and the methods of recording and also the method of transmitting these parameters.
- To study about the various assist devices used in the hospitals.
- To gain knowledge about equipment used for physical medicine and the various recently developed diagnostic and therapeutic techniques.

UNIT I ELECTRO-PHYSIOLOGY AND BIO-POTENTIAL RECORDING 9

The origin of Bio-potentials; biopotential electrodes, biological amplifiers, ECG, EEG, EMG, PCG, lead systems and recording methods, typical waveforms and signal characteristics.

UNIT II BIO-CHEMICAL AND NON ELECTRICAL PARAMETER MEASUREMENT 9

pH, PO_2, PCO_2, colorimeter, Auto analyzer, Blood flow meter, cardiac output, respiratory measurement, Blood pressure, temperature, pulse, Blood Cell Counters.

UNIT III ASSIST DEVICES 9

Cardiac pacemakers, DC Defibrillator, Dialyser, Heart lung machine

UNIT IV PHYSICAL MEDICINE AND BIOTELEMETRY 9

Diathermies- Shortwave, ultrasonic and microwave type and their applications, Surgical Diathermy Telemetry principles, frequency selection, biotelemetry, radiopill, electrical safety

UNIT V RECENT TRENDS IN MEDICAL INSTRUMENTATION 9

Thermograph, endoscopy unit, Laser in medicine, cryogenic application, Introduction to telemedicine

TOTAL: 45 PERIODS

OUTCOMES:

Upon completion of the course, students will be able to:

- Discuss the application of electronics in diagnostic and therapeutic area.
- Measure biochemical and various physiological information.
- Describe the working of units which will help to restore normal functioning.

TEXT BOOKS:

1. Leslie Cromwell, "Biomedical Instrumentation and Measurement", Prentice Hall of India, New Delhi, 2007.
2. John G.Webster, "Medical Instrumentation Application and Design", 3rd Edition, Wiley India Edition, 2007

REFERENCES:

1. Khandpur, R.S., "Handbook of Biomedical Instrumentation", TATA Mc Graw-Hill, New Delhi, 2003.
2. Joseph J.Carr and John M.Brown, "Introduction to Biomedical Equipment Technology", John Wiley and Sons, New York, 2004.

PTEC6002 ADVANCED DIGITAL SIGNAL PROCESSING L T P C
3 0 0 3

OBJECTIVES:

- To bring out the concepts related to stationary and non-stationary random signals
- To emphasize the importance of true estimation of power spectral density
- To introduce the design of linear and adaptive systems for filtering and linear prediction
- To introduce the concept of wavelet transforms in the context of image processing

UNIT I DISCRETE-TIME RANDOM SIGNALS 9

Discrete random process – Ensemble averages, Stationary and ergodic processes, Autocorrelation and Autocovariance properties and matrices, White noise, Power Spectral Density, Spectral Factorization, Innovations Representation and Process, Filtering random processes, ARMA, AR and MA processes.

UNIT II SPECTRUM ESTIMATION 9

Bias and Consistency, Periodogram, Modified periodogram, Blackman-Tukey method, Welch method, Parametric methods of spectral estimation, Levinson-Durbin recursion

UNIT III LINEAR ESTIMATION AND PREDICTION 9

Forward and Backward linear prediction, Filtering - FIR Wiener filter- Filtering and linear prediction, non-causal and causal IIR Wiener filters, Discrete Kalman filter.

UNIT IV ADAPTIVE FILTERS 9

Principles of adaptive filter – FIR adaptive filter – Newton's Steepest descent algorithm – LMS algorithm – Adaptive noise cancellation, Adaptive equalizer, Adaptive echo cancellers.

UNIT V WAVELET TRANSFORM 9

Multiresolution analysis, Continuous and discrete wavelet transform, Short Time Fourier Transform, Application of wavelet transform, Cepstrum and Homomorphic filtering.

TOTAL: 45 PERIODS

OUTCOMES:

Upon completion of the course, students will be able to:

- Explain the parametric methods for power spectrum estimation.
- Discuss adaptive filtering techniques using LMS algorithm and the applications of adaptive filtering.
- Analyze the wavelet transforms.

TEXTBOOKS:

1. Monson H, Hayes, "Statistical Digital Signal Processing and Modeling", John Wiley and Sons Inc., New York, Indian Reprint, 2007.
2. John G.Proakis, Dimitris G. Manolakis, "Digital Signal Processing", Pearson, Fourth 2007.
3. Dwight F. Mix, "Random Signal Processing", Prentice Hall, 1995.

REFERENCE:

1. Sophocles J. Orfanidis, "Optimum Signal Processing, An Introduction", Mc Graw Hill, 1990.

PT IT6005 DIGITAL IMAGE PROCESSING L T P C
3 0 0 3

OBJECTIVES:

The student should be made to:

- Learn digital image fundamentals.
- Be exposed to simple image processing techniques.
- Be familiar with image compression and segmentation techniques.
- Learn to represent image in form of features.

UNIT I DIGITAL IMAGE FUNDAMENTALS 8

Introduction – Origin – Steps in Digital Image Processing – Components – Elements of Visual Perception – Image Sensing and Acquisition – Image Sampling and Quantization – Relationships between pixels - color models.

UNIT II IMAGE ENHANCEMENT 10

Spatial Domain: Gray level transformations – Histogram processing – Basics of Spatial Filtering– Smoothing and Sharpening Spatial Filtering – **Frequency Domain:** Introduction to Fourier Transform – Smoothing and Sharpening frequency domain filters – Ideal, Butterworth and Gaussian filters.

UNIT III IMAGE RESTORATION AND SEGMENTATION 9

Noise models – Mean Filters – Order Statistics – Adaptive filters – Band reject Filters – Band pass Filters – Notch Filters – Optimum Notch Filtering – Inverse Filtering – Wiener filtering **Segmentation:** Detection of Discontinuities–Edge Linking and Boundary detection – Region based segmentation- Morphological processing- erosion and dilation.

UNIT IV WAVELETS AND IMAGE COMPRESSION 9

Wavelets – Subband coding - Multiresolution expansions - **Compression:** Fundamentals – Image Compression models – Error Free Compression – Variable Length Coding – Bit-Plane Coding – Lossless Predictive Coding – Lossy Compression – Lossy Predictive Coding – Compression Standards.

UNIT V IMAGE REPRESENTATION AND RECOGNITION 9

Boundary representation – Chain Code – Polygonal approximation, signature, boundary segments – Boundary description – Shape number – Fourier Descriptor, moments- Regional Descriptors – Topological feature, Texture - Patterns and Pattern classes - Recognition based on matching.

TOTAL: 45 PERIODS

OUTCOMES:

Upon successful completion of this course, students will be able to:

- Discuss digital image fundamentals.
- Apply image enhancement and restoration techniques.
- Use image compression and segmentation Techniques.
- Represent features of images.

TEXT BOOK

1. Rafael C. Gonzales, Richard E. Woods, "Digital Image Processing", Third Edition, Pearson Education, 2010.

REFERENCES:

1. Rafael C. Gonzalez, Richard E. Woods, Steven L. Eddins, "Digital Image Processing Using MATLAB", Third Edition Tata Mc Graw Hill Pvt. Ltd., 2011.
2. Anil Jain K. "Fundamentals of Digital Image Processing", PHI Learning Pvt. Ltd., 2011.
3. Willliam K Pratt, "Digital Image Processing", John Willey, 2002.
4. Malay K. Pakhira, "Digital Image Processing and Pattern Recognition", First Edition, PHI Learning Pvt. Ltd., 2011.
5. http://eeweb.poly.edu/~onur/lectures/lectures.html.
6. http://www.caen.uiowa.edu/~dip/LECTURE/lecture.html

PTGE6075 PROFESSIONAL ETHICS IN ENGINEERING L T P C

3 0 0 3

OBJECTIVES:

- To enable the students to create an awareness on Engineering Ethics and Human Values, to instill Moral and Social Values and Loyalty and to appreciate the rights of others.

UNIT I HUMAN VALUES 10

Morals, values and Ethics – Integrity – Work ethic – Service learning – Civic virtue – Respect for others – Living peacefully – Caring – Sharing – Honesty – Courage – Valuing time – Cooperation – Commitment – Empathy – Self confidence – Character – Spirituality – Introduction to Yoga and meditation for professional excellence and stress management.

UNIT II ENGINEERING ETHICS 9

Senses of 'Engineering Ethics' – Variety of moral issues – Types of inquiry – Moral dilemmas – Moral Autonomy – Kohlberg's theory – Gilligan's theory – Consensus and Controversy – Models of professional roles - Theories about right action – Self-interest – Customs and Religion – Uses of Ethical Theories

UNIT III ENGINEERING AS SOCIAL EXPERIMENTATION 9

Engineering as Experimentation – Engineers as responsible Experimenters – Codes of Ethics – A Balanced Outlook on Law.

UNIT IV SAFETY, RESPONSIBILITIES AND RIGHTS 9

Safety and Risk – Assessment of Safety and Risk – Risk Benefit Analysis and Reducing Risk - Respect for Authority – Collective Bargaining – Confidentiality – Conflicts of Interest – Occupational Crime – Professional Rights – Employee Rights – Intellectual Property Rights (IPR) – Discrimination.

UNIT V GLOBAL ISSUES 8

Multinational Corporations – Environmental Ethics – Computer Ethics – Weapons Development – Engineers as Managers – Consulting Engineers – Engineers as Expert Witnesses and Advisors – Moral Leadership –Code of Conduct – Corporate Social Responsibility

TOTAL: 45 PERIODS

OUTCOMES:

- Upon completion of the course, the student should be able to apply ethics in society, discuss the ethical issues related to engineering and realize the responsibilities and rights in the society

TEXT BOOKS:

1. Mike W. Martin and Roland Schinzinger, “Ethics in Engineering”, Tata Mc Graw Hill, New Delhi, 2003.
2. Govindarajan M, Natarajan S, Senthil Kumar V. S, “Engineering Ethics”, Prentice Hall of India, New Delhi, 2004.

REFERENCES:

1. Charles B. Fleddermann, “Engineering Ethics”, Pearson Prentice Hall, New Jersey, 2004.
2. Charles E. Harris, Michael S. Pritchard and Michael J. Rabins, “Engineering Ethics – Concepts and Cases”, Cengage Learning, 2009
3. John R Boatright, “Ethics and the Conduct of Business”, Pearson Education, New Delhi, 2003
4. Edmund G Seebauer and Robert L Barry, “Fundamentals of Ethics for Scientists and Engineers”, Oxford University Press, Oxford, 2001
5. Laura P. Hartman and Joe Desjardins, “Business Ethics: Decision Making for Personal Integrity and Social Responsibility” Mc Graw Hill education, India Pvt. Ltd.,New Delhi 2013.
6. World Community Service Centre, “Value Education”, Vethathiri publications, Erode, 2011

Web sources:

1. www.onlineethics.org
2. www.nspe.org
3. www.globalethics.org
4. www.ethics.org

PTGE6083 DISASTER MANAGEMENT L T P C
3 0 0 3

OBJECTIVES:

- To provide students an exposure to disasters, their significance and types.
- To ensure that students begin to understand the relationship between vulnerability, disasters, disaster prevention and risk reduction
- To gain a preliminary understanding of approaches of Disaster Risk Reduction (DRR)
- To enhance awareness of institutional processes in the country and
- To develop rudimentary ability to respond to their surroundings with potential disaster response in areas where they live, with due sensitivity

UNIT I INTRODUCTION TO DISASTERS 9

Definition: Disaster, Hazard, Vulnerability, Resilience, Risks – Disasters: Types of disasters – Earthquake, Landslide, Flood, Drought, Fire etc - Classification, Causes, Impacts including social, economic, political, environmental, health, psychosocial, etc.- Differential impacts- in terms of caste, class, gender, age, location, disability - Global trends in disasters: urban disasters, pandemics, complex emergencies, Climate change- Dos and Don'ts during various types of Disasters.

UNIT II APPROACHES TO DISASTER RISK REDUCTION (DRR) 9

Disaster cycle - Phases, Culture of safety, prevention, mitigation and preparedness community based DRR, Structural- nonstructural measures, Roles and responsibilities of- community, Panchayati Raj Institutions/Urban Local Bodies (PRIs/ULBs), States, Centre, and other stake-holders- Institutional Processess and Framework at State and Central Level- State Disaster Management Authority(SDMA) – Early Warning System – Advisories from Appropriate Agencies.

UNIT III INTER-RELATIONSHIP BETWEEN DISASTERS AND DEVELOPMENT 9

Factors affecting Vulnerabilities, differential impacts, impact of Development projects such as dams, embankments, changes in Land-use etc.- Climate Change Adaptation- IPCC Scenario and Scenarios in the context of India - Relevance of indigenous knowledge, appropriate technology and local resources.

UNIT IV DISASTER RISK MANAGEMENT IN INDIA 9

Hazard and Vulnerability profile of India, Components of Disaster Relief: Water, Food, Sanitation, Shelter, Health, Waste Management, Institutional arrangements (Mitigation, Response and Preparedness, Disaster Management Act and Policy - Other related policies, plans, programmes and legislation – Role of GIS and Information Technology Components in Preparedness, Risk Assessment, Response and Recovery Phases of Disaster – Disaster Damage Assessment.

UNIT V DISASTER MANAGEMENT: APPLICATIONS AND CASE STUDIES AND FIELD WORKS 9

Landslide Hazard Zonation: Case Studies, Earthquake Vulnerability Assessment of Buildings and Infrastructure: Case Studies, Drought Assessment: Case Studies, Coastal Flooding: Storm Surge Assessment, Floods: Fluvial and Pluvial Flooding: Case Studies; Forest Fire: Case Studies, Man Made disasters: Case Studies, Space Based Inputs for Disaster Mitigation and Management and field works related to disaster management.

TOTAL: 45 PERIODS

OUTCOMES:

The students will be able to

- Differentiate the types of disasters, causes and their impact on environment and society
- Assess vulnerability and various methods of risk reduction measures as well as mitigation.
- Draw the hazard and vulnerability profile of India, Scenarious in the Indian context, Disaster damage assessment and management

TEXTBOOK:

1. Singhal J.P. "Disaster Management", Laxmi Publications, 2010. ISBN-10: 9380386427 ISBN-13: 978-9380386423
2. Tushar Bhattacharya, "Disaster Science and Management", McGraw Hill India Education Pvt. Ltd., 2012. **ISBN-10:** 1259007367, **ISBN-13:** 978-1259007361]
3. Gupta Anil K, Sreeja S. Nair. Environmental Knowledge for Disaster Risk Management, NIDM, New Delhi, 2011
4. Kapur Anu Vulnerable India: A Geographical Study of Disasters, IIAS and Sage Publishers, New Delhi, 2010.

REFERENCES

1. Govt. of India: Disaster Management Act , Government of India, New Delhi, 2005
2. Government of India, National Disaster Management Policy,2009.

PTEC6703 EMBEDDED AND REAL TIME SYSTEMS L T P C
3 0 0 3

OBJECTIVES:

The student should be made to:

- Learn the architecture and programming of ARM processor.
- Be familiar with the embedded computing platform design and analysis.
- Be exposed to the basic concepts of real time Operating system.
- Learn the system design techniques and networks for embedded systems

UNIT I INTRODUCTION TO EMBEDDED COMPUTING AND ARM PROCESSORS 9

Complex systems and micro processors– Embedded system design process –Design example: Model train controller- Instruction sets preliminaries - ARM Processor – CPU: programming input and output-supervisor mode, exceptions and traps – Co-processors- Memory system mechanisms – CPU performance- CPU power consumption.

UNIT II EMBEDDED COMPUTING PLATFORM DESIGN 9

The CPU Bus-Memory devices and systems–Designing with computing platforms – consumer electronics architecture – platform-level performance analysis - Components for embedded programs-Models of programs- Assembly, linking and loading – compilation techniques- Program level performance analysis – Software performance optimization – Program level energy and power analysis and optimization – Analysis and optimization of program size- Program validation and testing.

UNIT III PROCESSES AND OPERATING SYSTEMS 9

Introduction – Multiple tasks and multiple processes – Multirate systems- Preemptive real-time operating systems- Priority based scheduling- Interprocess communication mechanisms – Evaluating operating system performance- power optimization strategies for processes – Example Real time operating systems-POSIX-Windows CE.

UNIT V SYSTEM DESIGN TECHNIQUES AND NETWORKS 9

Design methodologies- Design flows - Requirement Analysis – Specifications-System analysis and architecture design – Quality Assurance techniques- Distributed embedded systems – MPSoCs and shared memory multiprocessors.

UNIT V CASE STUDY 9

Data compressor - Alarm Clock - Audio player - Software modem-Digital still camera - Telephone answering machine-Engine control unit – Video accelerator.

TOTAL: 45 PERIODS

OUTCOMES:

Upon completion of the course, students will be able to:

- Describe the architecture and programming of ARM processor.
- Outline the concepts of embedded systems
- Explain the basic concepts of real time Operating system design.
- Use the system design techniques to develop software for embedded systems
- Differentiate between the general purpose operating system and the real time operating system
- Model real-time applications using embedded-system concepts

TEXT BOOK:

1. Marilyn Wolf, “Computers as Components - Principles of Embedded Computing System Design”, Third Edition “Morgan Kaufmann Publisher (An imprint from Elsevier), 2012.

REFERENCES:

1. Jonathan W.Valvano, “Embedded Microcomputer Systems Real Time Interfacing”, Third Edition Cengage Learning, 2012.
2. David. E. Simon, “An Embedded Software Primer”, 1st Edition, Fifth Impression, Addison-Wesley Professional, 2007.
3. Raymond J.A. Buhr, Donald L.Bailey, “An Introduction to Real-Time Systems- From Design to Networking with C/C++”, Prentice Hall, 1999.
4. C.M. Krishna, Kang G. Shin, “Real-Time Systems”, International Editions, Mc Graw Hill 1997
5. K.V.K.K.Prasad, “Embedded Real-Time Systems: Concepts, Design & Programming”, Dream Tech Press, 2005.
6. Sriram V Iyer, Pankaj Gupta, “Embedded Real Time Systems Programming”, Tata Mc Graw Hill, 2004.

PTEC6013 ADVANCED MICROPROCESSORS AND MICROCONTROLLERS L T P C

3 0 0 3

OBJECTIVES:

- To expose the students to the fundamentals of microprocessor architecture.
- To introduce the advanced features in microprocessors and microcontrollers.
- To enable the students to understand various microcontroller architectures.

UNIT I HIGH PERFORMANCE CISC ARCHITECTURE – PENTIUM 9

CPU Architecture- Bus Operations – Pipelining – Brach predication – floating point unit- Operating Modes –Paging – Multitasking – Exception and Interrupts – Instruction set – addressing modes – Programming the Pentium processor.

UNIT II HIGH PERFORMANCE RISC ARCHITECTURE – ARM 9

Arcon RISC Machine – Architectural Inheritance – Core & Architectures - Registers – Pipeline - Interrupts – ARM organization - ARM processor family – Co-processors - ARM instruction set- Thumb Instruction set - Instruction cycle timings - The ARM Programmer's model – ARM Development tools – ARM Assembly Language Programming - C programming – Optimizing ARM Assembly Code – Optimized Primitives.

UNIT III ARM APPLICATION DEVELOPMENT 9

Introduction to DSP on ARM –FIR filter – IIR filter – Discrete fourier transform – Exception handling – Interrupts – Interrupt handling schemes- Firmware and bootloader – Embedded Operating systems – Integrated Development Environment- STDIO Libraries – Peripheral Interface – Application of ARM Processor - Caches – Memory protection Units – Memory Management units – Future ARM Technologies.

UNIT IV MOTOROLA 68HC11 MICROCONTROLLERS 9

Instruction set addressing modes – operating modes- Interrupt system- RTC-Serial Communication Interface – A/D Converter PWM and UART.

UNIT V PIC MICROCONTROLLER 9

CPU Architecture – Instruction set – interrupts- Timers- I^2C Interfacing –UART- A/D Converter –PWM and introduction to C-Compilers.

TOTAL: 45 PERIODS

OUTCOME:

- The student will be able to work with suitable microprocessor / microcontroller for a specific real world application.

TEXT BOOK:

1. Andrew N.Sloss, Dominic Symes and Chris Wright " ARM System Developer's Guide : Designing and Optimizing System Software" , First edition, Morgan Kaufmann Publishers, 2004.

REFERENCES:

1. Steve Furber , "ARM System –On –Chip architecture", Addision Wesley, 2000.
2. Daniel Tabak , "Advanced Microprocessors", Mc Graw Hill. Inc., 1995
3. James L. Antonakos , " The Pentium Microprocessor", Pearson Education, 1997.
4. Gene .H.Miller, "Micro Computer Engineering", Pearson Education , 2003.
5. John .B.Peatman , "Design with PIC Microcontroller", Prentice Hall, 1997.
6. James L.Antonakos, "An Introduction to the Intel family of Microprocessors", Pearson Education, 1999.
7. Barry.B.Brey,"The Intel Microprocessors Architecture, Programming and Interfacing", PHI,2002.
8. Valvano, "Embedded Microcomputer Systems", Thomson Asia PVT LTD first reprint 2001. Readings: Web links www.ocw.nit.edu www.arm.com

PTEC6012 CMOS ANALOG IC DESIGN L T P C

3 0 0 3

OBJECTIVES:

- To study designs with better precision in data conversion
- To study various ADC and DAC circuit architectures

UNIT I SAMPLE AND HOLD 9

Properties of MOS Switches, multiplexed input architectures, recycling architecture, open and closed loop sampling architectures, switched capacitor and current mode architectures.

UNIT II BUILDING BLOCK OF DATA CONVERSION CIRCUITS: 9

Amplifiers, open loop and closed loop amplifiers, gain boosting, common mode feedback, bipolar, CMOS and BiCMOS comparators.

UNIT III PRECISION TECHNIQUES 9

Comparator cancellation, input and output offset storage principles, comparators using offset cancelled latches, opamp offset cancellation, ADC and DAC calibration techniques.

UNIT IV ADC/DAC ARCHITECTURES 9

DAC Performance metrics, reference multiplication and division, switching and logical functions of DACs, Current steering architectures, DAC Performance metrics, Flash ADC architecture, Gray encoding, thermometer encoding and metastability.

UNIT V OVER SAMPLING CONVERTERS 9

Delta sigma modulators, alternative modulator architectures, quantization and noise shaping, decimation filtering, implementation of Delta sigma modulators, delta sigma DACs,

TOTAL: 45 PERIODS

OUTCOMES:

Upon completion of the course, the student should be able to:

- Build Data Conversion circuits.
- Discuss calibration techniques
- Analyze ADC/DAC Architecture and Performance

TEXT BOOK:

1. B.Razavi “Data Conversion System Design” IEEE Press and John Wiley, 1995.

REFERENCE:

1. Phillip Allen and Douglas Holmberg “CMOS Analog Circuit Design” Second Edition, Oxford University Press, 2004.

PTEC6003 ROBOTICS AND AUTOMATION L T P C
3 0 0 3

OBJECTIVES:

- To study the various parts of robots and fields of robotics.
- To study the various kinematics and inverse kinematics of robots.
- To study the Euler, Lagrangian formulation of Robot dynamics.
- To study the trajectory planning for robot.
- To study the control of robots for some specific applications.

UNIT I BASIC CONCEPTS 9

Definition and origin of robotics – different types of robotics – various generations of robots – degrees of freedom – Asimov's laws of robotics – dynamic stabilization of robots.

UNIT II POWER SOURCES AND SENSORS 9

Hydraulic, pneumatic and electric drives – determination of HP of motor and gearing ratio – variable speed arrangements – path determination – micro machines in robotics – machine vision – ranging – laser – acoustic – magnetic, fiber optic and tactile sensors.

UNIT III MANIPULATORS, ACTUATORS AND GRIPPERS 9

Construction of manipulators – manipulator dynamics and force control – electronic and pneumatic manipulator control circuits – end effectors – U various types of grippers – design considerations.

UNIT IV KINEMATICS AND PATH PLANNING 9

Solution of inverse kinematics problem – multiple solution jacobian work envelop – hill Climbing Techniques – robot programming languages

UNIT V CASE STUDIES 9

Mutiple robots – machine interface – robots in manufacturing and non- manufacturing applications – robot cell design – selection of robot.

TOTAL: 45 PERIODS

OUTCOMES:

Upon completion of the course, the student should be able to:

- Explain the basic concepts of working of robot
- Analyze the function of sensors in the robot
- Write program to use a robot for a typical application
- Use Robots in different applications

TEXT BOOKS:

1. Mikell P. Weiss G.M., Nagel R.N., Odraj N.G., "Industrial Robotics", Mc Graw-Hill Singapore, 1996.
2. Ghosh, Control in Robotics and Automation: Sensor Based Integration, Allied Publishers, Chennai, 1998.

REFERENCES:

1. Deb. S.R.,"Robotics Technology and flexible Automation", John Wiley, USA 1992.
2. Klafter R.D., Chimielewski T.A., Negin M., "Robotic Engineering – An integrated approach", Prentice Hall of India, New Delhi, 1994.
3. Mc Kerrow P.J. "Introduction to Robotics", Addison Wesley, USA, 1991.
4. Issac Asimov "Robot", Ballantine Books, New York, 1986.
5. Barry Leatham - Jones, "Elements of industrial Robotics" PITMAN Publishing, 1987.
6. Mikell P.Groover, Mitchell Weiss, Roger N.Nagel Nicholas G.Odrey, "Industrial Robotics Technology, Programming and Applications ", McGraw Hill Book Company 1986.
7. Fu K.S. Gonzaleaz R.C. and Lee C.S.G., "Robotics Control Sensing, Vision and Intelligence" McGraw Hill International Editions, 1987.

PTCS6013 FOUNDATION SKILLS IN INTEGRATED PRODUCT DEVELOPMENT L T P C 3 0 0 3

OBJECTIVE:

This program can be offered with all Undergraduate programs/courses for all engineering streams. The FSIPD program aims to improve student's awareness and understanding of the basic concepts involved in Integrated product Development (IPD) by providing exposure to the key product development concepts. Students, who complete this program, will stand a better chance to be considered for jobs in the Engineering

industry.

COURSE OBJECTIVES:

After completing this program, the student will be able to obtain the technical skills needed to effectively play the entry level design engineer role in an engineering organization.

The student will be able to:

- Understand the global trends and development methodologies of various types of products and services
- Conceptualize, prototype and develop product management plan for a new product based on the type of the new product and development methodology integrating the hardware, software, controls, electronics and mechanical systems
- Understand requirement engineering and know how to collect, analyze and arrive at requirements for new product development and convert them in to design specification
- Understand system modeling for system, sub-system and their interfaces and arrive at the optimum system specification and characteristics
- Gain knowledge of the Innovation & Product Development process in the Business Context

UNIT I FUNDAMENTALS OF PRODUCT DEVELOPMENT 9

Global Trends Analysis and Product decision - Social Trends - Technical Trends- Economical Trends - Environmental Trends - Political/Policy Trends - Introduction to Product Development Methodologies and Management - Overview of Products and Services - Types of Product Development - Overview of Product Development methodologies - Product Life Cycle - Product Development Planning and Management

UNIT II REQUIREMENTS AND SYSTEM DESIGN 9

Requirement Engineering - Types of Requirements - Requirement Engineering - Traceability Matrix and Analysis - Requirement Management - System Design & Modeling - Introduction to System Modeling - System Optimization - System Specification - Sub-System Design - Interface Design

UNIT III DESIGN AND TESTING 9

Conceptualization - Industrial Design and User Interface Design - Introduction to Concept generation Techniques – Challenges in Integration of Engineering Disciplines - Concept Screening & Evaluation - Detailed Design - Component Design and Verification – Mechanical, Electronics and Software Subsystems - High Level Design/Low Level Design of S/W Program - Types of Prototypes, S/W Testing- Hardware Schematic, Component design, Layout and Hardware Testing – Prototyping - Introduction to Rapid Prototyping and Rapid Manufacturing - System Integration, Testing, Certification and Documentation

UNIT IV SUSTENANCE ENGINEERING AND END-OF-LIFE (EOL)SUPPORT 9

Introduction to Product verification processes and stages - Introduction to Product validation processes and stages - Product Testing standards and Certification - Product Documentation - Sustenance - Maintenance and Repair – Enhancements - Product EoL - Obsolescence Management - Configuration Management - EoL Disposal

UNIT V BUSINESS DYNAMICS ENGINEERING SERVICES INDUSTRY 9

The Industry - Engineering Services Industry - Product development in Industry versus Academia - The IPD Essentials - Introduction to vertical specific product development processes - Manufacturing/Purchase and Assembly of Systems - Integration of Mechanical, Embedded and S/W systems – Product development Trade-offs - Intellectual

Property Rights and Confidentiality - Security and configuration management.

TOTAL: 45 PERIODS

COURSE OUTCOMES:

The students will be able to

- Define, formulate and analyze a problem
- Solve specific problems independently or as part of a team
- Develop documentation, test specifications and coordinate with various teams to validate and sustain up to the EoL (End of Life) support activities for engineering customer
- Work independently as well as in teams
- Manage a project from start to finish

COURSE MATERIAL AND PEDAGOGY:

- NASSCOM has agreed to prepare / revise the course materials [selected teachers Anna University from major disciplines will be included in the process] as PPT slides for all theUNITS. The PPTs can be printed and given to each student if necessary at a Nominal Fee. This is the best possible material for this special course.
- NASSCOM will train the teachers of Anna University to enable them to teach this course. Atraining programme for nearly 3500 teachers needs to be organized. The team is exploring use of technology including the EDUSAT facility at Anna University.
- The course is to be offered as an elective to all UG Students both in the Constituent Colleges and Affiliated colleges of Anna University.

TEXT BOOKS [INDIAN ECONOMY EDITIONS]:

1. Karl T Ulrich and Stephen D Eppinger, "Product Design and Development", TataMcGraw Hill, Fifth Edition, New Delhi, 2011
2. John W Newstorm and Keith Davis, "Organizational Behavior", Tata McGraw Hill,Eleventh Edition, New Delhi, 2005.

REFERENCES:

1. Hiriyappa B, “Corporate Strategy – Managing the Business”, Authorhouse, USA, 2013
2. Peter F Drucker, “People and Performance”, Butterworth – Heinemann [Elsevier],Oxford, UK, 2004.
3. Vinod Kumar Garg and Venkitakrishnan N K, “Enterprise Resource Planning – Conceptsand Practice”, Prentice Hall India, New Delhi, 2003
4. Mark S Sanders and Ernest J McCormick, "Human Factors in Engineering and Design",McGraw Hill Education, Seventh Edition, New Delhi, 2013.

PTCS6003 AD HOC AND SENSOR NETWORKS L T P C

3 0 0 3

OBJECTIVES:

The student should be made to:

- Understand the design issues in ad hoc and sensor networks.
- Learn the different types of MAC protocols.
- Be familiar with different types of adhoc routing protocols.
- Be expose to the TCP issues in adhoc networks.
- Learn the architecture and protocols of wireless sensor networks.

UNIT I INTRODUCTION 9

Fundamentals of Wireless Communication Technology – The Electromagnetic Spectrum – Radio propagation Mechanisms – Characteristics of the Wireless Channel -mobile ad hoc networks (MANETs) and wireless sensor networks (WSNs) :concepts and architectures. Applications of Ad Hoc and Sensor networks. Design Challenges in Ad hoc and Sensor Networks.

UNIT II MAC PROTOCOLS FOR AD HOC WIRELESS NETWORKS 9

Issues in designing a MAC Protocol- Classification of MAC Protocols- Contention based protocols-Contention based protocols with Reservation Mechanisms- Contention based protocols with Scheduling Mechanisms – Multi channel MAC-IEEE 802.11

UNIT III ROUTING PROTOCOLS AND TRANSPORT LAYER IN AD HOC WIRELESS NETWORKS 9

Issues in designing a routing and Transport Layer protocol for Ad hoc networks- proactive routing, reactive routing (on-demand), hybrid routing- Classification of Transport Layer solutions-TCP over Ad hoc wireless Networks.

UNIT IV WIRELESS SENSOR NETWORKS (WSNS) AND MAC PROTOCOLS 9

Single node architecture: hardware and software components of a sensor node - WSN Network architecture: typical network architectures-data relaying and aggregation strategies -MAC layer protocols: self-organizing, Hybrid TDMA/FDMA and CSMA based MAC- IEEE 802.15.4.

UNIT V WSN ROUTING, LOCALIZATION & QOS 9

Issues in WSN routing – OLSR- Localization – Indoor and Sensor Network Localization-absolute and relative localization, triangulation-QOS in WSN-Energy Efficient Design-Synchronization-Transport Layer issues.

TOTAL: 45 PERIODS

OUTCOMES:

Upon completion of the course, the student should be able to:

- Explain the concepts, network architectures and applications of ad hoc and wireless sensor networks
- Analyze the protocol design issues of ad hoc and sensor networks
- Design routing protocols for ad hoc and wireless sensor networks with respect to some protocol design issues
- Evaluate the QoS related performance measurements of ad hoc and sensor networks

TEXT BOOK:

1. C. Siva Ram Murthy, and B. S. Manoj, "Ad Hoc Wireless Networks: Architectures and Protocols ", Prentice Hall Professional Technical Reference, 2008.

.

REFERENCES:

1. Carlos De Morais Cordeiro, Dharma Prakash Agrawal “Ad Hoc & Sensor Networks: Theory and Applications”, World Scientific Publishing Company, 2006.
2. Feng Zhao and Leonides Guibas, "Wireless Sensor Networks", Elsevier Publication - 2002.
3. Holger Karl and Andreas Willig “Protocols and Architectures for Wireless Sensor Networks”, Wiley, 2005
4. Kazem Sohraby, Daniel Minoli, & Taieb Znati, “Wireless Sensor Networks-Technology, Protocols, and Applications”, John Wiley, 2007.
5. Anna Hac, “Wireless Sensor Network Designs”, John Wiley, 2003.

PTEC6009 ADVANCED COMPUTER ARCHITECTURE L T P C
3 0 0 3

OBJECTIVES:

The student should be made to:

- Understand the micro-architectural design of processors
- Learn about the various techniques used to obtain performance improvement and power savings in current processors

UNIT I FUNDAMENTALS OF COMPUTER DESIGN 9

Review of Fundamentals of CPU, Memory and IO – Trends in technology, power, energy and cost, Dependability - Performance Evaluation

UNIT II INSTRUCTION LEVEL PARALLELISM 9

ILP concepts – Pipelining overview - Compiler Techniques for Exposing ILP – Dynamic Branch Prediction – Dynamic Scheduling – Multiple instruction Issue – Hardware Based Speculation – Static scheduling - Multi-threading - Limitations of ILP – Case Studies.

UNIT III DATA-LEVEL PARALLELISM 9

Vector architecture – SIMD extensions – Graphics Processing units – Loop level parallelism.

UNIT IV THREAD LEVEL PARALLELISM 9

Symmetric and Distributed Shared Memory Architectures – Performance Issues –Synchronization – Models of Memory Consistency – Case studies: Intel i7 Processor, SMT & CMP Processors

UNIT V MEMORY AND I/O 9

Cache Performance – Reducing Cache Miss Penalty and Miss Rate – Reducing Hit Time – Main Memory and Performance – Memory Technology. Types of Storage Devices – Buses – RAID – Reliability, Availability and Dependability – I/O Performance Measures.

TOTAL: 45 PERIODS

OUTCOMES:

At the end of the course, the student should be able to:

- Evaluate performance of different architectures with respect to various parameters
- Analyze performance of different ILP techniques
- Identify cache and memory related issues in multi-processors

TEXT BOOK:

1. John L Hennessey and David A Patterson, “Computer Architecture A Quantitative Approach”, Morgan Kaufmann/ Elsevier, Fifth Edition, 2012.

REFERENCES:

1. Kai Hwang and Faye Briggs, “Computer Architecture and Parallel Processing”, Mc Graw-Hill International Edition, 2000.
2. Sima D, Fountain T and Kacsuk P, ”Advanced Computer Architectures: A Design Space Approach”, Addison Wesley, 2000.

PTCS6012	SOFT COMPUTING	L T P C
		3 0 0 3

OBJECTIVES:

The student should be made to:

- Learn the various soft computing frame works
- Be familiar with design of various neural networks
- Be exposed to fuzzy logic
- Learn genetic programming.
- Be exposed to hybrid systems.

UNIT I INTRODUCTION **9**

Artificial neural network: Introduction, characteristics- learning methods – taxonomy – Evolution of neural networks- basic models - important technologies - applications.

Fuzzy logic: Introduction - crisp sets- fuzzy sets - crisp relations and fuzzy relations: cartesian product of relation - classical relation, fuzzy relations, tolerance and equivalence relations, non-iterative fuzzy sets. Genetic algorithm- Introduction - biological background - traditional optimization and search techniques - Genetic basic concepts.

UNIT II NEURAL NETWORKS **9**

McCulloch-Pitts neuron - linear separability - hebb network - supervised learning network: perceptron networks - adaptive linear neuron, multiple adaptive linear neuron, BPN, RBF, TDNN- associative memory network: auto-associative memory network, hetero-associative memory network, BAM, hopfield networks, iterative autoassociative memory network & iterative associative memory network – unsupervised learning networks: Kohonen self organizing feature maps, LVQ – CP networks, ART network.

UNIT III FUZZY LOGIC **9**

Membership functions: features, fuzzification, methods of membership value assignments- Defuzzification: lambda cuts - methods - fuzzy arithmetic and fuzzy measures: fuzzy arithmetic - extension principle - fuzzy measures - measures of fuzziness -fuzzy integrals - fuzzy rule base and approximate reasoning : truth values and tables, fuzzy propositions, formation of rules-decomposition of rules, aggregation of fuzzy rules, fuzzy reasoning-fuzzy inference systems-overview of fuzzy expert system-fuzzy decision making.

UNIT IV GENETIC ALGORITHM **9**

Genetic algorithm and search space - general genetic algorithm – operators - Generational cycle - stopping condition – constraints - classification - genetic programming – multilevel optimization – real life problem- advances in GA

UNIT V HYBRID SOFT COMPUTING TECHNIQUES & APPLICATIONS **9**

Neuro-fuzzy hybrid systems - genetic neuro hybrid systems - genetic fuzzy hybrid and fuzzy genetic hybrid systems - simplified fuzzy ARTMAP - Applications: A fusion approach of multispectral images with SAR, optimization of traveling salesman problem using genetic algorithm approach, soft computing based hybrid fuzzy controllers.

TOTAL : 45 PERIODS

OUTCOMES:

Upon completion of the course, the student should be able to:

- Apply various soft computing frame works.
- Design of various neural networks.
- Use fuzzy logic.
- Apply genetic programming.
- Discuss hybrid soft computing.

TEXT BOOKS:

1. J.S.R.Jang, C.T. Sun and E.Mizutani, “Neuro-Fuzzy and Soft Computing”, PHI / Pearson Education 2004.
2. S.N.Sivanandam and S.N.Deepa, "Principles of Soft Computing", Wiley India Pvt Ltd, 2011.

REFERENCES:

1. S.Rajasekaran and G.A.Vijayalakshmi Pai, "Neural Networks, Fuzzy Logic and Genetic Algorithm: Synthesis & Applications", Prentice-Hall of India Pvt. Ltd., 2006.

2. George J. Klir, Ute St. Clair, Bo Yuan, "Fuzzy Set Theory: Foundations and Applications" Prentice Hall, 1997.
3. David E. Goldberg, "Genetic Algorithm in Search Optimization and Machine Learning" Pearson Education India, 2013.
4. James A. Freeman, David M. Skapura, "Neural Networks Algorithms, Applications, and Programming Techniques, Pearson Education India, 1991.
5. Simon Haykin, "Neural Networks Comprehensive Foundation" Second Edition, Pearson Education, 2005.

PTEC6019 DATA CONVERTERS L T P C
3 0 0 3

OBJECTIVES:

- To explain the basic operational and design principles of CMOS Analog to Digital and Digital to Analog converter architectures.
- To introduce the design calculations for developing the various blocks associated with a typical CMOS AD or DA converter.
- To make students decide the dimensions and bias conditions of all the MOS transistors involved in the design.

UNIT I SAMPLE AND HOLD CIRCUITS 9

Sampling switches, Conventional open loop and closed loop sample and hold architecture, Open loop architecture with miller compensation, multiplexed input architectures, recycling architecture switched capacitor architecture.

UNIT II SWITCH CAPACITOR CIRCUITS AND COMPARATORS 9

Switched-capacitor amplifiers, switched capacitor integrator, switched capacitor common mode feedback. Single stage amplifier as comparator, cascaded amplifier stages as comparator, latched comparators.

UNIT III DIGITAL TO ANALOG CONVERSION 9

Performance metrics, reference multiplication and division, switching and logic functions in AC, Resistor ladder DAC architecture, current steering DAC architecture.

UNIT IV ANALOG TO DIGITAL CONVERSION 9

Performance metric, Flash architecture, Pipelined Architecture, Successive approximation architecture, Time interleaved architecture.

UNIT V PRECISION TECHNIQUES 9

Comparator offset cancellation, Op Amp offset cancellation, Calibration techniques, range overlap and digital correction.

TOTAL:45 PERIODS

OUTCOMES:

Upon completion of the course, the student should be able to:

- Explain sample and hold circuits
- Design ADC/DAC circuits
- Analyze ADC/DAC Architecture and Performance
- Discuss calibration techniques

TEXT BOOK:

1. Behzad Razavi, “Principles of data conversion System Design”, IEEE press, 1995.

REFERENCES:

1. Franco Maloberti, “Data Converters”, Springer, 2007.
2. Rudy Van de Plassche, “CMOS Integrated Analog-to-Digital and Digital-to-Analog Converters”, Kluwer Acedamic Publishers, Boston, 2003.

PTGE6084 **HUMAN RIGHTS** **L T P C**
3 0 0 3

OBJECTIVES :

- To sensitize the Engineering students to various aspects of Human Rights.

UNIT I **9**

Human Rights – Meaning, origin and Development. Notion and classification of Rights – Natural, Moral and Legal Rights. Civil and Political Rights, Economic, Social and Cultural Rights; collective / Solidarity Rights.

UNIT II **9**

Evolution of the concept of Human Rights Magana carta – Geneva convention of 1864. Universal Declaration of Human Rights, 1948. Theories of Human Rights.

UNIT III **9**

Theories and perspectives of UN Laws – UN Agencies to monitor and compliance.

UNIT IV **9**

Human Rights in India – Constitutional Provisions / Guarantees.

UNIT V **9**

Human Rights of Disadvantaged People – Women, Children, Displaced persons and Disabled persons, including Aged and HIV Infected People. Implementation of Human Rights – National and State Human Rights Commission – Judiciary – Role of NGO’s, Media, Educational Institutions, Social Movements.

TOTAL : 45 PERIODS

OUTCOMES:

- Engineering students will acquire the basic knowledge of human rights.

REFERENCES:

1. Kapoor S.K., “Human Rights under International law and Indian Laws”, Central Law Agency, Allahabad, 2014.
2. Chandra U., “Human Rights”, Allahabad Law Agency, Allahabad, 2014.
3. Upendra Baxi, The Future of Human Rights, Oxford University Press, New Delhi.

PTEC6007 **SPEECH PROCESSING** **L T P C**
3 0 0 3

OBJECTIVES:

- To introduce speech production and related parameters of speech.
- To show the computation and use of techniques such as short time Fourier transform, linear predictive coefficients and other coefficients in the analysis of speech.
- To understand different speech modeling procedures such as Markov and their implementation issues.

UNIT I BASIC CONCEPTS **10**

Speech Fundamentals: Articulatory Phonetics – Production and Classification of Speech Sounds; Acoustic Phonetics – Acoustics of speech production; Review of Digital Signal Processing concepts; Short-Time Fourier Transform, Filter-Bank and LPC Methods.

UNIT II SPEECH ANALYSIS **10**

Features, Feature Extraction and Pattern Comparison Techniques: Speech distortion measures– mathematical and perceptual – Log–Spectral Distance, Cepstral Distances, Weighted Cepstral Distances and Filtering, Likelihood Distortions, Spectral Distortion using a Warped Frequency Scale, LPC, PLP and MFCC Coefficients, Time Alignment and Normalization – Dynamic Time Warping, Multiple Time – Alignment Paths.

UNIT III SPEECH MODELING **8**

Hidden Markov Models: Markov Processes, HMMs – Evaluation, Optimal State Sequence – Viterbi Search, Baum-Welch Parameter Re-estimation, Implementation issues.

UNIT IV SPEECH RECOGNITION **8**

Large Vocabulary Continuous Speech Recognition: Architecture of a large vocabulary continuous speech recognition system – acoustics and language models – n-grams, context dependent sub-word units; Applications and present status.

UNIT V SPEECH SYNTHESIS **9**

Text-to-Speech Synthesis: Concatenative and waveform synthesis methods, sub-word units for TTS, intelligibility and naturalness – role of prosody, Applications and present status.

TOTAL: 45 PERIODS

OUTCOMES:

Upon completion of the course, students will be able to:

- Model speech production system and describe the fundamentals of speech.
- Extract and compare different speech parameters.
- Choose an appropriate statistical speech model for a given application.
- Design a speech recognition system.
- Use different speech synthesis techniques.

TEXTBOOKS:

1. Lawrence Rabiner and Biing-Hwang Juang, “Fundamentals of Speech Recognition”, Pearson Education, 2003.
2. Daniel Jurafsky and James H Martin, “Speech and Language Processing – An Introduction to Natural Language Processing, Computational Linguistics, and Speech Recognition”, Pearson Education, 2002.
3. Frederick Jelinek, “Statistical Methods of Speech Recognition”, MIT Press, 1997.

REFERENCES:

1. Steven W. Smith, “The Scientist and Engineer’s Guide to Digital Signal Processing”, California Technical Publishing, 1997.
2. Thomas F Quatieri, “Discrete-Time Speech Signal Processing – Principles and Practice”, Pearson Education, 2004.
3. Claudio Becchetti and Lucio Prina Ricotti, “Speech Recognition”, John Wiley and Sons, 1999.
4. Ben Gold and Nelson Morgan, “Speech and Audio Signal Processing, Processing and Perception of Speech and Music”, Wiley- India Edition, 2006.

PTEC6011 ELECTROMAGNETIC INTERFERENCE AND COMPATIBILITY L T P C
3 0 0 3

OBJECTIVES:

- To tutor the basics of EMI,EMC
- To instill knowledge on the EMI coupling mechanism and its mitigation techniques
- To impart comprehensive insight about the current EMC standards and about various measurement techniques

UNIT I BASIC THEORY 8

Introduction to EMI and EMC, Intra and inter system EMI, Elements of Interference, Sources and Victims of EMI, Conducted and Radiated EMI emission and susceptibility, Case Histories, Radiation hazards to humans, Various issues of EMC, EMC Testing categories, EMC Engineering Application.

UNIT II COUPLING MECHANISM 9

Electromagnetic field sources and Coupling paths, Coupling via the supply network, Common mode coupling, Differential mode coupling, Impedance coupling, Inductive and Capacitive coupling, Radiative coupling, Ground loop coupling, Cable related emissions and coupling, Transient sources, Automotive transients.

UNIT III EMI MITIGATION TECHNIQUES 10

Working principle of Shielding and Murphy’s Law, LF Magnetic shielding, Apertures and shielding effectiveness, Choice of Materials for H, E, and free space fields, Gasketting and sealing, PCB Level shielding, Principle of Grounding, Isolated grounds, Grounding strategies for Large systems, Grounding for mixed signal systems, Filter types and operation, Surge protection devices, Transient protection.

UNIT IV STANDARDS AND REGULATION 9

Need for Standards, Generic/General Standards for Residential and Industrial environment, Basic Standards, Product Standards, National and International EMI Standardizing Organizations; IEC, ANSI, FCC, AS/NZS, CISPR, BSI, CENELEC, ACEC. Electro Magnetic Emission and susceptibility standards and specifications, MIL461E Standards.

UNIT V EMI TEST METHODS AND INSTRUMENTATION 9

Fundamental considerations, EMI Shielding effectiveness tests, Open field test, TEM cell for immunity test, Shielded chamber , Shielded anechoic chamber, EMI test receivers, Spectrum analyzer, EMI test wave simulators, EMI coupling networks, Line impedance stabilization networks, Feed through capacitors, Antennas, Current probes, MIL -STD test methods, Civilian STD test methods.

TOTAL: 45 PERIODS

OUTCOMES:

Upon completion of the course, students will be able to:

- Find solution to EMI Sources, EMI problems in PCB level / Subsystem and system level design.
- To measure emission immunity level from different systems to couple with the prescribed EMC standards

TEXT BOOK:

1. Clayton Paul, “Introduction to Electromagnetic Compatibility”, Wiley Interscience, 2006

REFERENCES:

1. V Prasad Kodali, “Engineering Electromagnetic Compatibility”, IEEE Press, New york, 2001.
2. Henry W. Ott, “Electromagnetic Compatibility Engineering”, John Wiley & Sons Inc, New york, 2009
3. Daryl Gerke and William Kimmel, “EDN’s Designer’s Guide to Electromagnetic Compatibility”, Elsevier Science & Technology Books, 2002
4. W Scott Bennett, “Control and Measurement of Unintentional Electromagnetic Radiation”, John Wiley & Sons Inc., (Wiley Interscience Series) 1997.
5. Dr Kenneth L Kaiser, “The Electromagnetic Compatibility Handbook”, CRC Press 2005,

PTEC6018 MULTIMEDIA COMPRESSION AND COMMUNICATION L T P C

3 0 0 3

OBJECTIVES:

- To have a complete understanding of error–control coding.
- To understand encoding and decoding of digital data streams.
- To introduce methods for the generation of these codes and their decoding techniques.
- To have a detailed knowledge of compression and decompression techniques.
- To introduce the concepts of multimedia communication.

UNIT I MULTIMEDIA COMPONENTS 9

Introduction - Multimedia skills - Multimedia components and their characteristics - Text, sound, images, graphics, animation, video, hardware.

UNIT II AUDIO AND VIDEO COMPRESSION 9

Audio compression–DPCM-Adaptive PCM –adaptive predictive coding-linear Predictive coding-code excited LPC-perpetual coding Video compression –principles-H.261-H.263-MPEG 1, 2, and 4.

UNIT III TEXT AND IMAGE COMPRESSION 9

Compression principles-source encoders and destination encoders-lossless and lossy compression-entropy encoding –source encoding -text compression –static Huffman coding dynamic coding –arithmetic coding –Lempel ziv-welsh Compression-image compression

UNIT IV VOIP TECHNOLOGY 9

Basics of IP transport, VoIP challenges, H.323/ SIP –Network Architecture, Protocols,Call establishment and release, VoIP and SS7, Quality of Service- CODEC Methods- VOIP applicability

UNIT V MULTIMEDIA NETWORKING 9

Multimedia networking -Applications-streamed stored and audio-making the best Effort service-protocols for real time interactive Applications-distributing multimedia-beyond best effort service-secluding and policing Mechanisms-integrated services-differentiated Services-RSVP.

TOTAL : 45 PERIODS

OUTCOMES:

Upon Completion of the course, the students will be able to

- Describe various multimedia components
- Describe compression and decompression techniques.
- Apply the compression concepts in multimedia communication.

TEXT BOOK:

1. Fred Halshall “Multimedia communication - Applications, Networks, Protocols and Standards”, Pearson Education, 2007.

REFERENCES:

1. Tay Vaughan, “Multimedia: Making it work”, 7th Edition, TMH 2008 98
2. Kurose and W.Ross “Computer Networking “a Top Down Approach”, Pearson Education 2005
3. Marcus Goncalves “Voice over IP Networks”, Mc Graw hill 1999.
4. KR. Rao,Z S Bojkovic, D A Milovanovic, “Multimedia Communication Systems: Techniques, Standards, and Networks”, Pearson Education 2007.
5. R. Steimnetz, K. Nahrstedt, “Multimedia Computing, Communications and Applications”, Pearson Education Ranjan Parekh, “Principles of Multimedia”, TMH 2007.

PTEC6004 SATELLITE COMMUNICATION L T P C

3 0 0 3

OBJECTIVES:

- To understand the basics of satellite orbits.
- To understand the satellite segment and earth segment.
- To analyze the various methods of satellite access.
- To understand the applications of satellites**.**

UNIT I SATELLITE ORBITS 9

Kepler’s Laws, Newton’s law, orbital parameters, orbital perturbations, station keeping, geo stationary and non Geo-stationary orbits – Look Angle Determination- Limits of visibility –eclipse-Sub satellite point –Sun transit outage-Launching Procedures - launch vehicles and propulsion.

UNIT II SPACE SEGMENT AND SATELLITE LINK DESIGN 9

Spacecraft Technology- Structure, Primary power, Attitude and Orbit control, Thermal control and Propulsion, communication Payload and supporting subsystems, Telemetry, Tracking and command. Satellite uplink and downlink Analysis and Design, link budget, E/N calculation- performance impairments-system noise, inter modulation and interference, Propagation Characteristics and Frequency considerations- System reliability and design lifetime.

UNIT III EARTH SEGMENT 9

Introduction – Receive – Only home TV systems – Outdoor unit – Indoor unit for analog (FM) TV – Master antenna TV system – Community antenna TV system – Transmit – Receive earth stations – Problems – Equivalent isotropic radiated power – Transmission losses – Free-space transmission – Feeder losses – Antenna misalignment losses – Fixed atmospheric and ionospheric losses – Link power budget equation – System noise – Antenna noise – Amplifier noise temperature – Amplifiers in cascade – Noise factor – Noise temperature of absorptive networks – Overall system noise temperature – Carrierto- Noise ratio – Uplink – Saturation flux density – Input back off – The earth station - HPA – Downlink – Output back off – Satellite TWTA output – Effects of rain – Uplink rain– Fade margin – Downlink rain – Fade margin – Combined uplink and downlink C/N ratio – Inter modulation noise.

UNIT IV SATELLITE ACCESS 9

Modulation and Multiplexing: Voice, Data, Video, Analog – digital transmission system, Digital video Brocast, multiple access: FDMA, TDMA, CDMA, Assignment Methods, Spread Spectrum communication, compression – encryption.

UNIT V SATELLITE APPLICATIONS 9

INTELSAT Series, INSAT, VSAT, Mobile satellite services: GSM, GPS, INMARSAT, LEO, MEO, Satellite Navigational System. Direct Broadcast satellites (DBS)- Direct to home Broadcast (DTH), Digital audio broadcast (DAB)- Worldspace services, Business TV(BTV), GRAMSAT, Specialized services – E –mail, Video conferencing, Internet.

TOTAL: 45 PERIODS

OUTCOMES:

Upon Completion of the course, the students will be able to:

- Analyze the satellite orbits.
- Analyze the earth segment and space segment.
- Design various satellite applications

TEXT BOOK:

1. Dennis Roddy, “Satellite Communication”, 4th Edition, Mc Graw Hill International, 2006.

REFERENCES:

1. Wilbur L.Pritchard, Hendri G. Suyderhoud, Robert A. Nelson, “Satellite Communication Systems Engineering”, Prentice Hall/Pearson, 2007.
2. N.Agarwal, “Design of Geosynchronous Space Craft”, Prentice Hall, 1986.
3. Bruce R. Elbert, “The Satellite Communication Applications”, Hand Book, Artech House Bostan London, 1997.
4. Tri T. Ha, “Digital Satellite Communication”, II nd edition, 1990.
5. Emanuel Fthenakis, “Manual of Satellite Communications”, Mc Graw Hill Book Co., 1984.
6. Robert G. Winch, “Telecommunication Trans Mission Systems”, Mc Graw-Hill Book Co., 1983.
7. Brian Ackroyd, “World Satellite Communication and earth station Design”, BSP professional Books, 1990.
8. G.B.Bleazard, “Introducing Satellite communications“, NCC Publication, 1985.
9. M.Richharia, “Satellite Communication Systems-Design Principles”, Macmillan 2003.